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(54) A software protection system using a single-key cryptosystem, a hardware-based authorization system and a secure coprocessor.

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(73) Proprietor : **International Business Machines Corporation**
Old Orchard Road
Armonk, N.Y. 10504 (US)

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(72) Inventor : **Chandra, Ashileshwari Narain**
Pigott Road
Mahopac, N.Y. 10541 (US)
Inventor : **Comerford, Liam David**
Box 191, Rd. No. 1
10 Valley Road
Carmel, N.Y. 10512 (US)
Inventor : **White, Steve Richard**
7 Park Avenue, Apt. 33
New York, N.Y. 10016 (US)

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(74) Representative : **Burt, Roger James, Dr. et al**
IBM United Kingdom Limited
Intellectual Property Department
Hursley Park
Winchester Hampshire SO21 2JN (GB)

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Description

The invention is in the field of data processing and is specifically directed to a software copy protection mechanism. In particular a mechanism is provided which restricts software, distributed on magnetic disk or other medium, to use on any computer which is associated with a specified, physically secure coprocessor where the mechanism does not interfere with the user creation of "backup" copies, but the protection is not compromised by any such "backup" copies.

A good description of the background art to the invention is provided in the applicants EP-A-0 174 472.

In addition to the prior art identified in the referenced application, Uchenick U.S. Patent No. 4,458,315 suggests a security procedure for protecting software in which the software is distributed with a key number (first key information). In order for the software to be run on a computer, the computer must have access to a device storing "second key information". The program will not properly execute unless the first and second key information bears some specified relation. The patent however does not describe use of a transaction token which forms an important component of the present invention, nor is there any apparent impediment to deter a pirate from merely altering the software so it can execute without this comparison step nor does the patent describe how the computer obtains access to the "record key information" in such a way that that access cannot be duplicated or forged.

Best, in U.S. Patents 4,168,396; 4,278,837 and 4,465,901, describes a program protection mechanism which makes use of enciphered programs. However, Best suggests that each copy of a program be customized so that it is only executable on a single microprocessor, i.e. in a single, physically unique microprocessor, i.e. in a single, physically unique microprocessor. This protection appears breakable by discovering the key needed to decrypt the software and by other cryptanalytic techniques. However, as a practical technique for protecting software, it is unusable. A software vendor must sell each copy of his software (which has been customized), along with the microprocessor which has been customized to run the associated software. The only alternative is for the vendor to identify which microprocessor the user owns so that the software can be customized for it. To the extent that the software vendor allows the software to be run on a class of microprocessors (say all 80286), then the protection is lost, for duplicates of the software will freely run on all microprocessors of the class.

EP-A-0 191 162 discloses a cryptographic method wherein each copy of the program is encrypted with a key unique to that copy. The user of the program has a password unique to that user. The user's

password may be implemented in a smart card or as a password entered at a keyboard. Each copy of the program is unique.

The referenced EP-A-0 174 472 describes a method for recording data on magnetic media in such a way that the act of reading the data alters it. Such "read once" magnetic recordings cannot be created by conventional disk drives so they cannot be copied by conventional personal computers systems. As described in the referenced application a computer is associated with a physically and logically secure coprocessor and software as distributed on the magnetic media includes at least a significant section or portion which is encrypted. The magnetic media also includes an encrypted decryption key which must be used by the coprocessor to decrypt the encrypted portion of the software. The act of transferring the encrypted decryption key from the magnetic media to the secure coprocessor is linked to the mechanism which alters the data. This assures that, in the absence of extraordinary measures, the encrypted decryption key is not transferable once the magnetic medium has been read. This prevents the creation of copies of the distribution medium which can be employed on other computers, subverting the copy protection mechanism. Once the coprocessor has access to the encrypted decryption key, it can decrypt this key. In the course of executing the software the encrypted portion is decrypted by the coprocessor. This ensures that the software is executable, but prevents the user from obtaining access to the complete software package in decrypted form.

The mechanism described in the referenced application is extremely resistant to attack because the decryption key and the protected fraction of the software are never exposed to the user in unencrypted form. The owner of the software is allowed unlimited backup copies, but these backup copies are useless on any other personal computer (one which does not have access to the specific coprocessor already storing the decryption key). The protection system is open to any software writer for use and any hardware producer for manufacture because it requires no sharing of confidential information (key information) between the involved parties and its methods may be disclosed publicly without compromising its security.

However the read once magnetic recording has two vulnerabilities in a copy protection system. First it is always possible (although costly) to examine the magnetic medium and produce a device which will forge its behavior to a computer system. Second, a determined pirate may be able to build an apparatus which will restore the pre-read state of the "read once" recorded data that has been used to authorize the acceptance of a decryption key by a system. The re-initialized medium could then be used to illicitly "authorize" a second system.

The method, as described in the referenced ap-

plication requires use of a public key crypto system. This architectural restriction limits the flexibility of the system. In addition the system described in the referenced application requires the use of a bus slot which may not be available on some systems or may be too valuable to a user. This is necessary to allow the coprocessor to observe the host processor bus operations so that it may assure itself that the appearance of the read once magnetic recording is not being simulated by a program on the host computer.

The present invention overcomes the above noted disadvantages in that it firstly does not require a dedicated bus slot for a physically secure coprocessor, it does not rely on public key crypto systems nor does it rely on read once magnetic media. In these respects it is both more versatile and more resistant to attempted piracy than the earlier system.

The present invention is based on the recognition that today's software distribution techniques distribute to the user, in addition to the software itself, the right to execute that software. That is, more particularly, when software is sold to the typical user, the user acquires not only the software itself but the right to use it. However, he can, with a typical personal computer, duplicate the software and distribute it along with the (implied) right to use it to others. The invention seeks to separate the software, from the right to use it. Further, it seeks to place the means of creating these Rights-to-Execute in the hands of the software authors or their representatives. It also seeks to do these things with no perturbation to the existing or planned channels of software distribution and minimum change to the means by which software is prepared for distribution. As will be described below, in accordance with the present invention, the typical software purchaser may still duplicate at will the software he has received from the vendor. However, he cannot duplicate the right to use the software; in fact he receives a single right to use the software. To become effective, that right to use must be installed in a suitable coprocessor, and it is only when the right to use is installed on the suitable coprocessor (which is associated with the host computer on which the user intends to run the software) that the software becomes executable. Other copies of the software that the user might make are not executable on any other host computer (even is such other host computer is associated with another suitable coprocessor).

In accordance with the present invention software can be distributed on magnetic media (such as tape or floppy disk) or by other means (telephone lines, cable or broadcast transmission). The software is partitioned into an encrypted portion, P_e , and an unencrypted (clear text) portion P_c . The choice of the partitioning is made by the software vendor with the understanding that only the encrypted portion will be protected from piracy. The encrypted portion, P_e , of the software will be decrypted and executed by a

physically and logically secure coprocessor if the coprocessor possesses the decryption key which embodies the right to execute. The protected part of the software is, thus, never exposed in plaintext form and never executed by unauthorized systems. The coprocessor may be attached to the user's personal computer either on the system bus or through a I/O or other communication port.

In order to be effective, and decrypt the encrypted portion of the protected software, the coprocessor must be provided with the decryption key (Right-to-Execute or RTE, also referred to as AK or Application Key) needed to render the encrypted portion of software executable. The key (RTE or AK) must be transferred to the software owner's coprocessor in such a way that the transfer mechanism cannot be reused or reproduced by a user and thus grant key transfer to other personal computers. This is accomplished by associating the effective transfer of the decryption key into the non-volatile memory of the coprocessor with a transaction token, e.g. the presence of the transaction token is required to effectively transfer the decryption key to the coprocessor. The token is very resistant to forgery for reasons which are described below and its information content is destroyed during the transfer transaction. The token has information content which is known to the coprocessor because it is identified in an encrypted file. This file is called the Encrypted Token Description or ETD. This identification is authenticated to the coprocessor by the fact that it is encrypted with the same key (AK) which the software vendor has used to encrypt the protected portion of the software. The ETD may be distributed to the user by placing it in a dedicated register in the token, by recording it on the distribution medium, or by distributing it by other means.

The software vendor supplied key (AK) is made available to the coprocessor by supplying it, in encrypted form, with the program via the software distribution medium or other means as described in the case of the ETD. In the general case, the key used to encrypt the AK is selected from a list of Coprocessor Supervisor Keys (CSKs) which is stored in all coprocessors supplied by a given vendor.

The function of encrypting the software vendor's decryption key with one of these stored encryption keys is a service provided by the coprocessor so that the store of keys (CSKs) need never be exposed to the software vendor. The coprocessor which the software vendor uses to encrypt the software vendor's keys may be precisely the same type of coprocessor as the user employs to decrypt and run the software. Alternatively, special processors may be sold for this purpose or standard processors may be enabled to perform this function by an encrypted message from the hardware manufacturer. These options may be useful in giving flexibility to the marketer of such devices.

For the same reason the software vendor's decryption keys (such as AK) are never exposed to the software user, the hardware vendor's encryption keys (CSK) are never exposed to the software vendor. This system thus has the property that no information need be shared between hardware and software manufacturers in order to use the protection provided. In addition, a single encryption system such as DES may be used for all cryptographic functions.

In accordance with the invention, in order to access the transfer token, the coprocessor is provided with a data path which allows communication with a hardware sub-system such as a cartridge. This data path may utilize a connection to the cartridge supplied by either the host (such as a PC) or the coprocessor. The cartridge contains an electronic memory with properties that change (in a manner to be described) in response to whether the memory is being read from or written to. This cartridge sub-system must be extremely difficult to copy in the sense that a substitute is very unlikely to be created by a user which can fool a coprocessor into accepting it as verification of the user's right to store a given decryption key (AK) in the user's coprocessor. Since the connection to the sub-system is physically exposed, the transaction which uses this connection as its communications medium must be forgery-resistant.

To be forgery-resistant, each transaction must be effectively unique and verifiable by the coprocessor. The technique by which this security is obtained is described below.

The sub-system which is used to verify the right of a user to a particular decryption key is referred to as a transfer token. The necessary functions can be implemented using programs executed by a microprocessor or by using a dedicated hardware system. Inasmuch as the most economical implementation of a transfer token will be a dedicated hardware system, this is the version that is described.

The transfer token hardware and its information content must be kept physically secure to avoid its access by the user through means which bypass the forgery prevention feature. One implementation of this physical security is described in EP-A-0 268 882, the disclosure of which is incorporated by this reference. The same technique may be employed to render the coprocessor itself physically secure and to protect its memory contents from its exposure to a user. Since tokens can be implemented as a single, continuously powered, integrated circuit chip, no additional physical security is needed. If the physical security of a token were broken, then no cryptographic keys would be revealed and only the single associated software package could be redistributed.

Thus, in accordance with the invention, software to be protected is partitioned into a plain text portion and an encrypted portion; the encryption key (AK) used to encrypt the software is known only to the

software vendor. The protected software provided to the user is associated with the software decryption key, in encrypted form (EAK). The software decryption key (AK) is encrypted with the hardware vendor's encryption key (CSK) which is not known to the software vendor. This encryption is a service performed by such coprocessors. The software and encrypted decryption key (EAK) may be associated with information describing a transfer token (TOKEN DATA) by encrypting that description under AK. The correspondence between the content of the token and the token data (after decryption under AK) indicates to the coprocessor that that particular AK may be stored for future use. The authorized user is provided with a token, which in a preferred embodiment is implemented in the form of a hardware cartridge. The transfer token is physically and logically secure, so that a sufficient fraction of its information contents is not available to the user. The transfer token, or the hardware cartridge incorporating the transfer token has a read once feature whereby the first time the hardware cartridge is read the contents are altered so that the authorization which the hardware cartridge represents cannot be effectively reused by the user. The user is also provided with a physically and logically secure coprocessor. The physically and logically secure coprocessor has in permanent memory the hardware vendor's decryption key(s) CSK1, CSK2, etc. The data descriptive of the token will typically be transferred with the software.

The first time the user attempts to employ the protected software, he couples the physically and logically secure coprocessor to the computer system in which the protected software is to be run and also couples the hardware cartridge containing the transfer token to the computer system.

On the first running of the software the encrypted software key EAK is transferred to the coprocessor and is decrypted by the coprocessor using the required CSK to obtain AK. For reasons which are explained below, this transfer is temporary, the coprocessor will reject the transfer if the conditions described below are not met. A verifiable portion of the contents of the hardware cartridge, i.e., the transfer token, is also transferred to the coprocessor using a forgery-resistant query/response protocol; this process alters or destroys the contents of the hardware cartridge. The information revealed to an observer of the query and the response is insufficient to allow a forger to construct a correct response to another coprocessor query. The transfer token can be "refilled" with information by a software author, but the refilled transfer token will only authorize a coprocessor to accept an AK from that author. The physically and logically secure coprocessor determines whether or not the transfer token is effective by determining whether or not it corresponds to an expected transfer token. Assuming the transfer token is found acceptable, the

coprocessor then stores the software decryption key AK in its permanent memory. At this point the transfer is effective so that the coprocessor can use AK to decrypt and run protected software.

Once the software decryption key AK is stored in the permanent memory of the coprocessor, the protected software can be executed. The plaintext portion of the protected software is executed by the user computer system. The encrypted portion (the protected portion) of the software is decrypted upon loading by the coprocessor. The logical and physical security of the coprocessor memory prevents the user from having access to plaintext or executable form of the protected software.

On each subsequent use of the protected software the software decryption key AK now stored in the permanent memory of the coprocessor is used to decrypt the encrypted portions thereof. The user can make as many "backup" copies of the software as he desires; however without access to a logically and physically secure coprocessor storing the decryption key AK, any "backup" copies of the software are unusable since the encrypted portion of the software cannot be decrypted.

Typically, in conjunction with execution of the decrypted software by said coprocessor, the authorized processor will execute the remaining portions of the software, e.g. the originally unencrypted portions.

The distinct right-to-execute in a preferred embodiment comprises a decryption key for the encrypted portions of the software. While the decryption key may be distributed along with the software, it is distributed in encrypted form. The coprocessor is provided, during manufacture, with the key (CSK) required to decrypt the software decryption key. Without further protection, however, this would authorize any coprocessor to run any software distributed in accordance with the preceding description; this would not meet the purposes of software authors. Therefore, the coprocessor requires further evidence of the user's right-to-execute; that evidence takes the form of an authentic (unused) hardware cartridge. If and only if the hardware cartridge meets the challenge of the coprocessor will the software decryption key be rendered effective. The coprocessor has available to it (by one of several alternative routes) what is referred to as token data. This token data can be coupled to the coprocessor in encrypted form so that the path by which the encrypted token data is made available to the coprocessor need not be secure. The token data is encrypted using the same software decryption key. The cartridge stores, in clear text form, the token data, and hence the hardware cartridge must be physically and logically secure. The coprocessor challenge to the hardware cartridge is in the form of a query, the hardware cartridge responds to the query by returning a reply which is a function of both the query and the token data. This function does not

reveal enough about the token data to allow the behavior of an unused token to be reproduced. An example of such a function would be the selection of 50% of the token content on the basis of the query content. As the hardware cartridge generates the response, all the clear text token data which it had stored is overwritten or destroyed. As a result of the query/response process, the coprocessor receives the selected portion or transformation of the clear text token data. The process is arranged so that even though the communication path between the coprocessor and the hardware cartridge is not secure, someone monitoring the transaction (for example copying the query and the response) would still have insufficient information to simulate the behavior of the cartridge. While the coprocessor only receives the selected portion or a transformation of the clear text token data, since the coprocessor also has available to it both the query and the entire clear text token data (having decrypted the encrypted token data), the coprocessor has available to it all the information it needs to determine whether or not the response identifies an authentic cartridge.

25 The communication path between coprocessor and hardware cartridge can either be direct (by coupling the hardware cartridge to a port in the coprocessor) or be indirect via the host or processor to be authorized.

30 A preferred example of a suitable query is a random number, of specified bit length. The query is input to the hardware cartridge and is used to select among distinctive and exclusive portions of the clear text token data as stored in the hardware cartridge. In a simple example the query can be binary so that if the clear text token data is divided into two portions, each bit of the query can be used to select a bit from the first or the second portion. A selected bit makes up one bit of the response, and simultaneously the selected and unselected bits are overwritten or otherwise erased. In this example then, the bit length of the query is equal to half the bit length of the clear text token data. Because the communication path between the coprocessor and the hardware cartridge is unsecured, it is assumed that a pirate will be able to copy the query and response. But the pirate is incapable of controlling the query another coprocessor will generate and therefore the response which he is aware of is insufficient to allow him to simulate the effect of an authentic hardware cartridge.

50 The software, the associated encrypted decryption key EAK and information respecting authorized transfer token(s) may be provided on a magnetic medium (floppy disk or tape). Alternatively, that information can be provided via a communication link (telephone line, CATV, etc.). The coprocessor which is employed in accordance with the method need not be permanently attached to or hardwired into the user computer system. However, in order to execute the

protected software on the computer system, the computer system must be capable of communicating with the coprocessor. If the coprocessor does not yet store the software decryption key (AK), the coprocessor must also be associated with a transfer token so that the decryption key AK can be accepted by the coprocessor. The hardware cartridge containing the transfer token is arranged so that it is capable of authorizing an appropriate transfer of a decryption key AK on one and only one occasion. This assures that the user can transfer the software decryption key AK to a single coprocessor. Although the user may freely backup the software medium, the protected program can only be executed in cooperation with a coprocessor containing the software decryption key. Because the coprocessor is physically and logically secure, the stored software decryption key cannot be copied. Because the hardware cartridge containing the transfer token is both physically and logically secure it cannot be copied, and since the act of transferring the software encryption key causes the token information content to be altered or destroyed, this process can only be executed once.

As described above, the software distribution medium included information describing the token. This information is encrypted to protect it from exposure to unauthorized sources. However, as will be described below, it is not at all essential for the encrypted token description to be contained within or associated with the software distribution medium. It is an advantage if tokens are all unique. For a unique token description to be contained on each software distribution medium requires in effect that each software distribution medium be unique. It would be more advantageous if all software distribution media could be identical as this minimizes manufacturing costs. This can be achieved by providing the hardware cartridge with the means for storing the encrypted token data. All unique information can thus be isolated to the hardware cartridge.

As will be described below the transactions to be effected are varied in dependence on whether or not the encrypted token data is associated with the software distribution medium or the hardware cartridge containing the clear text token data.

In either case, the token will be tested by the coprocessor in such a way that the testing process (even though carried out via unsecured conductors) does not reveal sufficient token data to successfully forge the authorization which the token represents. To implement this process a "random" number is generated by the coprocessor (and retained by it for future use). The "random" number is used to interrogate the token which returns to the coprocessor that portion of the token data which is selected by the "random" number. At the cartridge, both the token data which is selected (and transferred to the coprocessor) as well as token data which is not selected is de-

stroyed. In this fashion, even if the response of the hardware cartridge is recorded by unauthorized individuals, it is useless except in the limited situation where the coprocessor generates the identical "random" number or the forger guesses the required reply.

For a forger to fool another coprocessor by presenting that coprocessor with a forged reply to its random number query, either the second query must be identical to the first, or the forger must guess the correct response to the new query. Both of these can be made less likely by using longer queries. By combining good random number generation with sufficiently long queries to and replies from the token, the job of forgery can be made arbitrarily hard.

The coprocessor accepts the encrypted software key EAK from either the token cartridge or the software distribution media (preferred) and decrypts it using a coprocessor supervisor key CSK stored in the coprocessor at the time of manufacture.

The coprocessor then accepts the encrypted token data (from either the token cartridge or the software distribution medium) and, using the decrypted software key AK, decrypts the token data. The coprocessor can then combine the "random" number and the clear text token data to determine the "correct" response of the hardware cartridge to its "random" number query. The "correct" response calculated by the coprocessor can then be compared with the actual response of the hardware cartridge, if they compare favorably, the right of the user to have use of the key AK has been established. This use does not include the ability to determine the actual content of AK.

In the event that the encrypted token data is not associated with the software distribution medium, it can instead be stored in a dedicated register in the hardware cartridge. The clear text token data is stored in a specialized register set which on reading will only provide a portion of the stored information where the provided information is selected by the response of the read components of the token on the basis of the selecting random number. These components may further transform the selected information using the part of the stored information which was not selected. Before or after the coprocessor has queried the hardware cartridge and received its response (the combination of "random number" and clear text token data), it can continue interrogating the hardware cartridge so as to read out the encrypted token data. Of course, this means that the encrypted form of the token data will be available to the user (and any unauthorized individual). But this no more undermines the security than having the encrypted token data on the software distribution medium where it too is available to the user and unauthorized individuals. It should be noted that the token contains no cryptographic keys. If the coprocessor is considered to be a trusted receiver of secret information (AKs) then the token can

be likened to a wax seal on an envelope which assures the receiver that no other receiver has accepted that message (an AK). The token, thus gives the software vendor control over the total number of coprocessors that are able to execute their software. It is this control which is absent at present and allows software piracy to exist.

It should be clear that this system's solution to the piracy problem is based on amending both the personal computer and the floppy disk by the addition of a hardware component which compensates for the exposures that allow piracy. In the case of the personal computer, that exposure is the availability of the complete address and register space of the computing system to user scrutiny and command. In the case of the floppy disk (or any other distribution or storage media) it is the fact that a functional reproduction of the media can be made. It is possible to describe the amendments in a manner which makes the applicability of this system to computing systems other than personal computers clearer.

The openness of the existing computing system is addressed by the addition of a coprocessing system. This system is needed to implement two levels of privilege above that of the user of the system. This is necessary even in the case that the computing system has a CPU which implements privilege. In such a system, some user is still responsible for loading the operating system and could provide an operating system which supports his access to protected portions of the vendor's code. Even if the operating system were burned in ROM in the CPU, the user would not be denied physical access to the protected code.

The privilege structure dictated by this problem is thus a branch on whatever structure is already present. This branch contains two levels which are effectively parallel and separate from the existing structure. The two levels of privilege which are added to the computer by the coprocessor (or are designed appropriately into the computing system) have distinct functions. The lowest of the two levels provides a space in which a portion of the vendor's code can be executed as a service for the part of the vendor's code executing in the open memory of the host computer. The higher level of privilege in the branch is used to perform the secure transactions which prevent proliferation of the rights-to-execute when they are installed or used. These transactions are available as a service to the lower level of privilege or the host.

This architectural extension of present computing systems provides support for data processing security functions in general by providing means for the trustworthy execution of software. The changed privilege structure provides an execution space in which the software provider has a privilege greater than the system user. This privilege level is parallel to, but distinct from, the system operator. At this privilege level

the software provider may execute his product as a service to the user without giving access to it as a software object. This software is then trustworthy in the sense that it cannot be changed or observed by the user, but only accessed as a service. The uses of such trustworthy execution will be obvious to those skilled in the system security area.

A second level of privilege is provided as part of this additional privilege structure. It is the responsibility of this level of privilege to control the operations needed to cause the software product to execute. This level of privilege is trustworthy in the sense it will only allow execution of code if it has been given the right to execute by the software provider. This level of privilege has the additional job of isolating applications executed at the lower level in the event that multiple programs are executed there. This is a classical operating system function and is necessary to keep code at the lower level trustworthy.

Accordingly the invention provides a method of securing protected software against unauthorized use without perturbing existing channels of software distribution and, at the same time, not interfering in users' creation of unlimited backup copies of protected software, said method comprising the steps of: a) providing to a user a physically secure coprocessor and coupling said coprocessor to a user host computer to support bidirectional communication therebetween to create a composite computing system including said host computer and said coprocessor; b) providing logical security to said coprocessor by: 1) providing a first privilege level including first level secure memory and first level operating instructions, secured against access or variation by said user, for executing protected software; c) distributing protected software in a form in which at least a portion is inexecutible by said host computer; d) distributing a further tangible element distinct from said protected software representing a right to execute said protected software; e) providing said composite computing system access to said protected software and to said further tangible element; characterised in that: step b) further comprises the step of: 2) providing a second privilege level including second level secure memory and second level operating instructions, secured against access or variation by said user or any author of protected software, for controlling authorization for execution of said protected software by said first privilege level; in that, in step c), the protected software distributed is executable by said coprocessor but only with authorization by said second privilege level; and in that the method further comprises the steps of: f) verifying authenticity of said further tangible element by said coprocessor at said second privilege level; g) altering said second level secure memory in a distinctive fashion to reflect a determination by said second privilege level of authenticity of said tangible element; and h) executing said protected software so long as

said alteration of said second privilege level secure memory is detected and denying said request if said alteration is not present.

From the preceding it should be apparent that the first privilege level has the function of executing protected software as a service to the software vendor and to the user, but protecting that software from access by the user. The second privilege level includes the key management functions of acquiring a right to execute and, once acquired, copying the necessary decryption key into the secure memory space (second privilege level) of the coprocessor. As distributed, of course, the protected software is inexecutable by the host computer since at least a portion is encrypted; the software is executable by the coprocessor once it has authorization by the second privilege level. The further tangible element is the hardware cartridge storing the transfer token. The manner by which the coprocessor verifies the authenticity of the transfer token has already been described in detail. The alteration to the second level secure memory is the writing therein of the software decryption key. Whenever execution of the protected software is requested by the user, access is made, via the coprocessor's second privilege level to the secure memory space to determine if the appropriate software decryption key is present; if present, the coprocessor initiates decryption of the protected software and storage of that software in the coprocessor's first level secure memory space. Subsequently, the second privilege level authorizes execution by the first privilege level. Of course, if the necessary software decryption key is not present, the user's request to execute the software is denied.

Brief Description of the Drawings

The invention will now be described in such detail to enable those skilled in the art to practice the same when taken in conjunction with the attached drawings in which like reference characters identify identical apparatus and in which:

Fig. 1 is a block diagram showing major components of the host computer 10 and the coprocessor 15;

Fig. 2 is a block diagram of the hardware cartridge 20 and its association with a suitable connector 23, either from the I/O port 154 of the coprocessor 15 or the I/O port 19 of the host 10;

Fig. 3 illustrates the components of a distributable software set in accordance with one embodiment of the invention;

Fig. 4 illustrates how the distributable components are created by the software vendor;

Fig. 5 schematically illustrates the interaction between the composite computing system consisting of the host 10 and the coprocessor 15, and the software distribution package consisting of

the token cartridge and the distributable software set (Fig. 3) during an acquire right-to-execute process;

Fig. 6 is similar in Fig. 5 except that it illustrates the interaction of the foregoing components subsequent to an Acquire Right-To-Execute process, that is, during a load, decrypt and run process; Fig. 7 is a flow diagram illustrating the services executed in the host in accordance with the invention;

Fig. 8 is a flow diagram of the Acquire Right-To-Execute process executed in the coprocessor 15; Fig. 9A illustrates the services executed in the host 10 in accordance with a Load Decrypt and Run process; and

Fig. 9B illustrates the corresponding services executed in the coprocessor in connection with the Load Decrypt and Run process.

20 Detailed Description of Preferred Embodiments

As described in the above referenced EP-A-0 174 472, the term Personal Computer, or single user system for individual work station, as applied to a class of small computers can be misleading. These systems are more properly termed Single Operator Systems. From the point of view of classical operating systems, this places the user-operator in a position of trust in which he has access to all of the system code and system facilities. In common Single Operator Systems, this exposure of system code and system hardware facilities is an opportunity to replicate and re-distribute code, which on a classical large computing system with separate operators and users, would be unavailable. The means by which this security is achieved on large systems is the implementation of a system in which users are given a privileged status level which is tested by the system to determine whether or not the user may execute certain instructions or access certain data for reading or modification.

It is the purpose of the present invention, in common with a purpose of the invention of the above referenced EP-A-0 174 472 to teach how a system utilizing privilege may be implemented on Single Operator Systems to the advantage of the hardware manufacturer, the software vendor, and the scrupulous Single Operator System User. This is referred to as a "Shared Higher Level of Privilege System" because it can be viewed as providing each software vendor with a higher privilege level than the user, without giving any vendor access to other vendors' privileged information. By using this system, a portion of hardware and software of the system is hidden in a coprocessor sub-system which is installed or connected to Single Operator System (also referred to as the host or work station) so that some portion of the vendor software can be made inaccessible to the user except as a ser-

vice. In addition the method for transporting software either on some magnetic media (floppy disk or tape) or via a communication link allows the software vendor to cryptographically hide some fraction of the software from the user in spite of the user being able to examine it with the resources available to him on the system.

For purpose of this description the host hardware or work station is assumed to be IBM Personal Computer (PC) the operation of which is described in the IBM Personal Computer technical reference manual, 1981, the host disk operating system (DOS) is assumed to be IBM PC DOS. This assumption is for the sake of clarity because the operation in DOS services of this combination are typical of a class of machines in which this invention is useful. It should be understood that these DOS and hardware operations are intended to be representative of all the analogous operations on this or other possible host systems (including main frame computers) under this or other operating systems.

A software copy protection system in accordance with the invention employing shared higher level of privilege is composed of two parts; the hardware privilege support system (a coprocessor) which is installed in or coupled to the work station, and the medium (magnetic or otherwise) which is used to transport the software from the vendor to the user. As will be described the form in which the software is communicated makes it easily copied for backup purposes. In order to obtain a coprocessor's future cooperation in executing the software, however, the software must at the first use be presented to the coprocessor with a token. The token cartridge can effectively be used only once and is very resistant to forgery. Copies of the magnetic media are useless for conveying the ability to execute the software package. Use of a token gives authorization to the coprocessor to accept a decryption key AK so that the software can be executed on the PC (or host) in conjunction with this particular coprocessor. Other coprocessors which have been authorized to use the same key may also execute the software. When the token cartridge is used, it becomes incapable of conveying subsequent authorizations. The software vendor is thus provided with the means for controlling the number of authorized systems without the need for two way communication with the systems.

The application software must consist of at least one file of encrypted program. This part of the application software is encrypted with a key (AK) provided by the software vendor. The software key (AK) is supplied to the user in encrypted form (EAK), encrypted under a key (CSK) known only to the hardware vendor. The decryption key (CSK) for that encrypted software key EAK is supplied by the hardware vendor in the secure memory of the coprocessor. Encryption of an AK to produce an EAK is carried out in such a way

5 that the hardware vendor's encryption key (CSK) is not available to the software vendor. It is in the best interest of the software vendor to encrypt those fractions of the application software which he considers proprietary, as it is the encrypted fraction of the software which will be protected from redistribution by the user.

10 In the event that the software is to be distributed by a magnetic medium (floppy disk or tape) then that medium which is ready to be sold or released may consist of the following (see Fig. 3):

1. An application which consists of at least one file of program encrypted with a key AK selected by the software vendor.
2. The decryption key AK needed to render the encrypted software executable provided in encrypted form EAK where the encryption is by the hardware vendor's (secret) encryption key CSK.
3. Token data TD which is in encrypted form, ETD encrypted with the same software vendor key (AK) which is used to encrypt the encrypted portion of the application.

15 As will be described below encryption of the software decryption key is carried out by the use of a coprocessor which may in all respects be identical to the coprocessor which is operated in conjunction with the PC. Because of the characteristic of the coprocessor, the software vendor who employs this equipment does not have access to the information (hardware vendor's encryption key CSK) stored therein.

20 25 30 35 As will be described below the software may also be distributed via a communication link, so long as the package of information which is transmitted to a host includes the components enumerated above.

35 40 It is a characteristic of the invention that the method cannot be defeated by duplication of the software and attempting to execute the information enumerated above, whether or not it is executed on a host which is or is not coupled to a coprocessor unless that coprocessor has received authorization to execute that software.

45 50 55 If a pirate wanted to make copies of the enumerated information which will operate on systems which are coupled to the coprocessor he must somehow provide for the transfer from the distribution medium to the coprocessor permanent storage of the software decryption key. However, as will be described below, this transfer requires token data from a use-once-hardware-cartridge and, as will be described, the hardware cartridge can be arranged so that it is arbitrarily difficult to duplicate or forge. In the absence of an appropriate token cartridge, the coprocessor will not accept the transfer of the AK which is necessary to run the protected program. Thus piracy by copying the information that is distributed is inhibited by the difficulty in duplicating or forging the behavior of an authorizing token cartridge.

If the pirate wishes to make a copy of the distrib-

uted information which will run on systems without the coprocessor, then he must first decrypt those parts of the application program which have been encrypted. Since there are two processors in coprocessor equipped systems, it is possible that the application may be written to operate concurrently on two processors, or use special facilities on the coprocessor. If so, the application must be drastically modified to be operational. Piracy by copying a decrypted version of the application is thus inhibited by strength of the encryption method used to render the application unreadable. It is practical to make this a virtually insurmountable task. Even if this were accomplished, the software could still be useless unless it were rewritten to run without coprocessor.

In order for the copy protection system to be effective, it is necessary not only that no copy can be made of the distribution information which will convey authorization to accept AK into another coprocessor, but also that the software must never reside in system memory in a form which allows the user to make a binary image of the system memory with a complete working version of the application which could be loaded in other systems.

It is also desirable that the user must be able to make unlimited quantities of backup copies of the software for which the user's coprocessor is authorized, all of which are useless on unauthorized systems.

These are features of this protection system and are supported by the coprocessor. The coprocessor is itself a computing system. It has its own processor, firmware and read only memory (ROM), a real time clock and RAM. It could be installed in a personal computer as an adapter card set mapped into the memory and I/O address space, or it could merely be coupled to a personal computer via an I/O port. It should be noted that it is a feature of the present invention, as contrasted with the invention described in the above referenced EP-A-0 174 472 that the coprocessor is portable. The coprocessor communicates with the PC in one of two different fashions. If the coprocessor is directly installed (such as a card set) in a PC, then it can communicate with the PC through a region of common memory and through a set of registers which reside in the port address space of the PC. In this version the common memory is part of the coprocessor. The coprocessor controls its bus transceivers and can cause the common memory to be unavailable to the PC for read operations. (This architecture is described in the referenced EP-A-0 174 472. In the alternative, the coprocessor can communicate with the PC through an I/O port. Regardless of the manner in which such communication is effected, it is necessary that only a portion of the coprocessor memory be addressable by the PC. It is also necessary that the portion of the coprocessor memory which is not addressable by the PC be physically in-

accessible to the user. It is this memory in which the coprocessor will decrypt and run the encrypted portion of the application software.

5 In addition to the processor, memory (RAM and ROM) and port address registers (if any) the coprocessor has physically and logically secure memory space which contains ROM and non-volatile memory devices (such as battery backed CMOS RAM or EEPROMs).

10 The ROM contains the system firmware. It is in the form of a monitor whose commands are the services which the PC may request from the coprocessor. A complete set of such services would include as a minimum set:

- 15 1. Acquire a Right-to-Execute (ARE).
2. Load, decrypt, and run application (LDR).

The non-volatile RAM device is used by the coprocessor as a secure non-volatile memory in which decryption keys AK1, AK2, etc. or initialized applications are stored along with all CSKs.

20 It should be understood that the coprocessor must have at least two levels of privilege so that the memory used to store AKs can be properly secured from the user. This is needed because the user could be a writer-of-software. If the software executed on behalf of one author could read the AKs of other authors, the user-authors could recover other authors' AKs and decrypt their protected software. Management of installation, use of, and access to AKs should be understood to be important functions of the higher level of privilege to the system.

25 All application software decrypted and run on the coprocessor is at a lower level of privilege than the ROM resident firmware which controls non-volatile RAM access and, loading, decrypting, and starting of protected software.

30 35 40 As noted earlier, the coprocessor must be physically as well as logically secure. This security is required in order to prevent the user from applying logic analyzers or other digital control and recording devices to gain a record of the content of the secure memory space and hence the AKs or decrypted software. A suitable packaging for the coprocessor is described in applicants EP-A-0,268,882, the disclosure of which is incorporated by this reference.

45 50 55 The PC or individual work station is a common single bus micro-processor system. The IBM PC is typical of this class of machines. Such a system uses the bus (which can be an array of transmission lines with sockets at intervals) as a communication medium between logically separate sub-systems. Some of the sub-systems may reside on the same packaging element (in this case a printed circuit board called the "System Board") that supports the bus. Sub-systems which are necessary to the function of the system or offer expansion of the function of the system are handled by attachments to the bus through the sockets. It should be noted that the components

which constitute a sub-system may be made so that parts of the sub-system may reside on different packaging elements.

The complement of sub-systems which are shown in figure 1 in the region labeled host, as indicated at 10, is an example of a common, nearly minimal, PC. The PC CPU 4 is a single chip micro-processor and a group of support chips. The PC CPU 4 is supplied with a periodic signal called the clock and with connection to the bus by the support chips. The micro-processor is commonly supplied with more support than this, but all support is aimed at executing a repeating cycle of fetches of instructions from memory, fetches of data from some selected element of the sub-systems (such as Random Access Memory), execution of instructions, and when needed, storage of resulting data in an selected element of the system. The CPU 4 may have support supplied to it called direct memory access (DMA) which allows the micro-processor to be relieved of tasks which involve the movement of data from one addressable element to another.

The micro-processor controls the type of bus operation performed (fetch, store, etc.) and the type of element selected (RAM, Port Address Register, etc.) by which of the controls lines in the bus is "asserted" (changed to the appropriate potential according to a protocol called the bus definition). By these means, the micro-processor is able to obtain a collection of instruction (called the program), execute the instruction on a set of data, and cause the data stored in other elements of the system to change as a consequence of the execution of the instructions.

The RAM 6 is a sub-system from which data can be fetched from and written to by the CPU 4. It is the sub-system used to store data and instruction which are loaded from some other source. If it has meaningful content, then that content has been written to it by the CPU 4. At the time that the computer is powered on, the RAM 6 contents are, for practical purposes, meaningless.

The ROM 8 is a sub-system from which data can only be fetched. It may contain a collection of programs which are needed to start useful operation of the computer and which are useful for controlling the remaining sub-systems.

The remaining sub-systems, terminal control unit 9, display 11, manual input device 13, disk system control unit 15, disk drive 17 and I/O port 19 can be characterized as having or supporting both addressable elements and mechanical, optical or electro-magnetic (or other) elements which can affect human senses, be affected by human actions, or manipulate a magnetic medium to perform read and write operations involving creating and sensing the boundary between magnetic domains on the magnetic medium. The contents of some of the addressable elements control the actions of the mechanical, optical, and

electro-magnetic effectors of the sub-system, and the contents of other addressable elements are controlled by the mechanical, optical or electro-magnetic elements. Thus, by these means, it is possible for the computer system to interact with the user and with magnetic and other media. The sub-system which provides the elements needed to interact with the user is called the terminal control sub-system. The common form of sub-system which allow read and write operations on magnetic medium are called disk control systems. Given these elements it is possible to describe in broad outline, the operation of such systems.

At the time of power up, the micro-processor executes an instruction fetch from a fixed location in memory. This address is one which is occupied by the ROM 8. The instruction in that location is a jump to the programs which have the effect of testing and initializing the system for use. One of the system initialization programs causes a program called the Disk Operating System (DOS) to be read from a disk and executed. This program (the DOS) is able to accept commands from the user through the use of the terminal control system. These commands include causing a program chosen by the user to execute on the system by naming the file (using the manual input) in which the program resides to the DOS program.

The complement of sub-systems which are shown in FIG. 1, as indicated at 15 is an example of a minimal coprocessor system. The elements of the hardware may be thought of as consisting of two parts. One part (at 154) contains addressable elements which allow the hardware to communicate with the PC so that commands and data may be exchanged (much as between a user and the host system). The other parts contain the coprocessor CPU 150, RAM 151, ROM 152 real time clock 156 and non-volatile RAM 153, not concerned directly with communicating with the PC.

The non-volatile RAM 153 may be implemented as EEPROM or as battery backed CMOS RAM or in any other technology which allows erasure of that memory's contents.

The combination of properties, non-volatility and eraseability are needed so that the software keys (AK's) and coprocessor supervisor keys (CSK) can be retained between uses of the coprocessor, but can be erased in the event that physical intrusion detection system 155 detects a tampering attempt; see EP-A-0 268 882 for an example of such a system.

A real time clock 156 is a sub-system which contains a specialized counter. It is supplied with power by a battery, typically the same battery as is used to power the nonvolatile memory and the tamper detector. The battery supplies power to the counter and its support chips during the period when the computing system is turned off. The counter increments its registers in response to clock signals generated by its

support chips so that its registers reflect the interval of time since the registers were initialized by the coprocessor manufacturer. Thus, if the registers were initialized to the time of day, then their contents would approximately track the time of day. The registers of the real time clock can be read by the CPU 150.

Figure 1 shows a configuration of the PC and associated coprocessor by which software distributed in accordance with the invention can be executed. For purposes of this description we will assume that the software is distributed on magnetic media such as a floppy disk, although as the description proceeds it will become apparent that the software may be distributed by any conventional technique. While in accordance with the invention described in the referenced EP-A-0 174 472 the Support Hardware communicated with the host through its internal bus, it is a feature of the present invention that the coprocessor can be coupled to the PC through a communication port, so that the coprocessor can be conveniently portable. We will describe operation of the system using this configuration although it should be apparent that the present invention can also be employed when the coprocessor communicates to the PC through an internal bus.

The coprocessor 15 has some features in common with the Support Hardware of the referenced EP-A-0-174 472. More particularly, the coprocessor provides each software vendor with an instance of a higher privilege level than the user but at the same time does not give any software vendor access to other vendors' privileged information. All application software decrypted and run on the coprocessor is at the lower of the two privilege levels; the higher privilege level, implemented in the ROM resident, firmware, controls access to the non-volatile RAM 153, loading, decrypting and running operations. This structure in the coprocessor prevents a software vendor from writing a monitor which would run on the coprocessor to access the firmware and non-volatile memory (including the decryption keys) and make that information available to the host 10.

Accordingly, the coprocessor 15 has a first or lower privilege level which has access to the RAM 151; as already described, the RAM 151 is secured from the user and/or the host 10. The first privilege level includes first level operating instructions for executing protected software. The coprocessor 15, however, also has defined a second privilege level including a second level secure memory and second level operating instructions. The second level secure memory is represented by the non-volatile RAM 153 and the second level operating instructions are defined in the ROM 152. The second privilege level is secured both against the user and any software author. It is the second privilege level of the coprocessor 15 which is involved in acquisition of rights to execute and therefore controls the procedures antecedent

thereto. The same second privilege level also responds to user requests for execution of protected software, provides for the loading and decryption of protected software and initiating the first privilege level into operation for execution of the protected software, but only in the event that the second privilege level determines that such execution is authorized.

There are, in accordance with the present invention, two modes of operating on or with protected software, a first mode called Acquire Right-to-Execute (ARE) is required to authorize the coprocessor to execute the protected application. Each coprocessor may be authorized to execute many software applications by performing the ARE transaction for each application. Thereafter, when the apparatus executes a software package for which it is authorized, it operates in a Load, Decrypt and Run (LDR) mode.

Figure 3 describes a software configuration which is employed in accordance with the present invention. As shown in Fig. 3 the three files may be distributed as unit (either on a magnetic medium or via a communication link). A first file is an encrypted software decryption key EAK. The second file is the software which includes both plain text software (PART 1) and protected or encrypted software (PART 2) (EAK (Software, Part 2)). The last file is encrypted token data EAK(TOKEN DATA). While the encrypted software no encrypted token data are encrypted using a common key (AK), such that each of them can be decrypted using the decryption key (AK), the software decryption key is encrypted with a different key (CSK); the hardware vendor's key which, as will be described, can be maintained secret from the software vendor. While in accordance with the invention described in the referenced EP-A-0 174 472, a key pair using a "public key" cryptographic system must be used to encrypt and decrypt the software key, it is a feature of this system that any adequately secure cryptographic system may be used including DES or other "symmetric" key systems in which the same key is used for encryption and decryption.

For purposes of this description we will assume that the three files of figure 3 are contained in floppy disk which is loaded in the disk drive 17. In order to initialize the coprocessor, a token cartridge 20 is coupled to the coprocessor 15 or to the host PC 10. The cartridge 20 includes token data stored in a memory device which has unique characteristics as will be described below. At this point it suffices to note that the cartridge 20 is arranged so that it can be used on a single occasion; the act of employing the cartridge 20 alters it such that it is no longer usable for its intended purpose.

In order to employ the (protected) software the coprocessor 15 must be provided with the decryption key (AK) which is needed to render the encrypted por-

tion of the application executable. This key will be transferred to the software owners coprocessor 15 in such a way that the transfer mechanism cannot be reused or reproduced. This is accomplished by requiring the presence of an unused token cartridge to effectively transfer the decryption key into the nonvolatile memory of the coprocessor 15. "Unused" in this context means only that the content of the token cartridge supplied by the software author has not been read by any means prior to this occasion. As noted the token is difficult to forge for reasons which are discussed later and is effectively erased in the transfer transaction. The token has information content which is identifiable to the coprocessor 15 either because it is enumerated in an encrypted file associated with the program (or alternatively provided by another source, as described below). The enumeration evidences the fact that this has been provided by the software author. It is authenticated to the coprocessor 15 by the fact that the information is encrypted with the same key (AK) which the software vendor has used to encrypt the protected portion of the application.

The cartridge 20, storing the transfer token is coupled to the I/O device 154 of the coprocessor (not illustrated) or the I/O device 19 of the PC (as illustrated) via a connector provided for that purpose. Since the connector to the cartridge 20 is exposed, and can therefore be monitored by the user, the transaction which uses the connector must be difficult to forge given that some of the data will be exposed. To have this property, each transaction should be effectively unique and verifiable by the coprocessor.

The cartridge 20 is both physically and logically secure. Physical security may be provided in a number of fashions, one of those fashions is described in the applicants EP-A-0 268 882. A preferable method of physical security is to implement the circuitry of the token as a single integrated circuit chip. The cartridge 20 includes memory which behaves in such a fashion which allows verification of no-prior-use as well as verification of its authenticity. Both verifications are needed by the coprocessor before the decryption key AK will be accepted for future use. As will be described below the cartridge 20 may be manufactured by a third party source so long as its connector and protocol are standardized. Its information content must be determined and loaded by the software author or the author's representative. Data is transferred from the cartridge to the coprocessor using a query/response protocol. The query is a random number, and it in combination with the token data determines the token response. The accessible information passing on the unsecured path between coprocessor 15 and cartridge 20 is the "random number" and the response of the cartridge, neither of which reveals the token data. The coprocessor has access to a copy of the token data (for example by decrypting

the encrypted token data from the software distribution media). Thus the coprocessor can independently determine the "correct" response, and can compare the actual response from the cartridge 20 with its own, independently determined "correct" response. Thus only the random query and the actual response are exposed during the transaction. The complete token information needed to combine with the query to obtain the response is not revealed. At the same time as the cartridge 20 produces its response, it also alters its contents so the cartridge cannot be reused. This is accomplished by providing a region of memory in cartridge 20 which does not behave as if it were a normal memory under read operation. (A block diagram with suitable architecture for the cartridge 20 is shown in figure 2). Briefly, the cartridge 20 includes at least two memory segments, both of which can be written to as if they were conventional serial input shift registers. When a read is performed, however, the access properties of the regions changes. During reads both memory segments are enabled and produce data at an output as would a normal serial output shift register. Both the outputs are sent to a multiplexer. The multiplexer selects which of the two (or more) segment's data to route to the connector (and thus to the coprocessor) by the state of a control line which is contained within the connector, and which is driven by the coprocessor with the "random number". When the memory region of the cartridge is read out, the contents in both segments are erased. This ensures that a pirate observing the transaction between the coprocessor and the token cartridge can only obtain a portion of the information content of the cartridge. This portion of the token information is sufficient to verify to the coprocessor that it is a valid token which can authorize the coprocessor to use the software package but it is far from sufficient to allow a pirate to reconstruct its original content in order to fool the coprocessor into accepting a key which is not rightfully owned.

In an embodiment of the cartridge 20 which was described above, two shift registers are employed, so that during a read, the selected 50% of the memory contents are exposed. In alternative versions larger memory segments, or larger granularity of selection than a bit, or using addressable stored data to respond to queries are suggested techniques. These variations offer trade-offs to the system designer concerned with cost versus security.

During this read operation a portion of the contents of the cartridge 20 is transferred to the coprocessor. The portions which are selected are determined by a "random" number generated by the coprocessor. Both the "random" number as well as the response from the cartridge 20 are then stored in the coprocessor. This information can then be compared with the token data (file 3-figure 3) which is also transferred to the coprocessor from the software dis-

tribution medium. Failure of the token data to correspond to the expected token data is taken as proof of forgery of the token cartridge and results in rejection by the coprocessor of the decryption key for future use, and of course only if the decryption key is accepted for future use can the protected software be executed.

Figure 2 is a block diagram of one embodiment of cartridge 20. In this embodiment the token device is implemented as a single silicon CMOS integrated circuit chip 25 for cost and physical security reasons. This chip is appropriately packaged so that the data storage elements 120, 220 are continuously powered by a battery 26. CMOS integrated circuits can be built with static power requirements so small that the data stored in these registers, if not read out, can be expected to be preserved for a period almost equal to the shelf life of the battery. In the case that the data is read, as will be described, the other components needed to affect reading are supplied with power from an external source through the external power and ground lines 27. As shown in figure 2 the cartridge 20 is coupled to the coprocessor or the host PC via connector 23 having Clock, Selection, Data Input, Data Output, External Power and External Ground lines. The cartridge 20 contains two memory segments in the form of Serial In, Serial Out, Shift Left, Shift Registers 120, 220, a first segment including cells 121-12n and a second segment including cells 221-22n. Shift registers of this kind have the property that the state of the bit stored in their left-most cells (121, 221) is reflected in the state of their output lines (D1, D2). They have the additional properties that when the falling edge of a clock pulse is presented at their clock lines (C1, C2) the state of each cell is changed to that of the cell to its immediate right so that the pattern of bits in the register is shifted to the left. In the case of the right-most cells (12n, 22n) the falling edge of the clock pulse causes these cells to assume the state of the data input lines (D3, D4). The cells can be filled with data by supplying a data bit at each of the two data input lines and then supplying a clock pulse. If this procedure is repeated for n clock pulses, then all n bits of the registers will be filled. An encrypted (under AK) copy of these bits could then be made and stored on a floppy disk to supply the encrypted description of the token data. This procedure is followed by a software author to prepare the authorization to a coprocessor to accept an AK.

When a read operation is performed, each bit of the coprocessor generated random number is placed consecutively on the select line. Each setting of the select line 21 is followed by a clock pulse. Both shift registers will shift left on each clock pulse. Data from the first shift register is placed on the line D1 and from the second shift register on the line D2; both of which are inputs to a multiplexer 22 which is in turn controlled by the select line 21 from the coprocessor or host

PC. The select line 21 determines which of the two signals D1 or D2 are coupled through the latch 24 to the output DATA. The latch is used to prevent a pirate from obtaining the token data by changing the select twice for each clock pulse. The consequence of this arrangement is that for each bit which is presented at the data out, two bits have been shifted out of the registers, and two bits, which are useless for authorization, have been shifted in.

Accordingly, and assuming that the entire memory contents of the cartridge 20 were read out, one observing the input to the select line 21 and DATA output, would only observe, at most, 50% of the contents of the cartridge 20. The coprocessor knows from the Encrypted token information exactly what bits should have appeared in that 50% so it has sufficient information to confirm the validity of that authorization, but a pirate lacking the destroyed 50% will not be able to forge an authorization.

When the coprocessor 15 is requested to acquire an AK (ARE), a process (see Fig. 5) is initiated. This begins when the encrypted software decryption key (file 1) and encrypted token data (file 3) are read into the RAM 151 or temporary memory 15T. In addition, a "random" number (3) is generated by the coprocessor and used in performing a read operation on the cartridge 20 as previously described. The "random" number is used to select which bits of which memory segments will be effective to pass the multiplexer 22. The coprocessor 15 stores the "random" number in RAM 151 along with the resulting DATA (4) from the cartridge 20.

It should be understood by those skilled in the art that there are a very large number of variations on the design of the token cartridge. All have the properties that the data read from the cartridge is transformed as a function of both the query bits and the token data content and that the resulting reply from the token is predictable if the complete content of the token is known.

The coprocessor decrypts the software decryption key $E_{CSK}(AK)$ and the resulting software decryption key (AK) is used to decrypt the token data to produce clear text token data. In the case that there are multiple CSKs, the encrypted software key could be supplied with a reference to the correct CSK in a header. Such headers could provide an index to the correct CSK in plain text or a recognition flag which decrypts to an expected pattern only if the correct CSK is used. Many other methods are possible. Having decrypted EAK with the correct CSK, the coprocessor can then combine the stored "random" number or query with the clear text token data and independently determine the "correct" response. The actual response (the DATA received from the cartridge 20) can then be compared to the "correct" response. If the two quantities compare favorably that is taken as an indication that the cartridge 20 authorizes the copro-

cessor to store AK (5) in nonvolatile RAM or permanent memory 15P for future use. The user can now successfully request that the coprocessor execute the software protected by the newly acquired AK. It should be noted that the key AK may be re-encrypted by the processor prior to storage. This step of re-encryption could provide improved protection of the stored key or could, if used correctly, support storage of the key outside the coprocessor.

On the other hand, if the "correct" and actual response of the cartridge do not compare favorably, the software decryption key (AK) is discarded and the coprocessor is unable to properly execute the encrypted software wherefore the application program will not run properly.

It should also be apparent that this transaction destroys the content of the cartridge 20 (see Fig. 6) so that it cannot be reused to authorize any other coprocessor to run the application, or any other application program.

The enormous number of possible selections of the contents of the different segments of the cartridge that can be made by the coprocessor and the amount of information in which the pirate must reconstruct by trial and error are the barriers to the pirate's successful reconstruction of the cartridge 20. The probability of successful forgery (P (Forgery)) is a function of the probability that the coprocessor will ask for the same selection at any given bit of the request (P (same)), the probability that the pirate will guess the "lost" data of an observed transaction correctly (P (guess)), and the number of bits requested by the coprocessor in the validation transactions (Numbits). $P(\text{forgery}) = (P(\text{same}) + (1 - P(\text{same}))(P(\text{guess})))$ to the power Numbits. If $P(\text{same}) = 0.5$ and $P(\text{guess}) = 0.5$ and Numbits = 128 as could easily be achieved in practice with a small integrated chip, then $P(\text{forgery})$ is approximately ten to the -16th power. If the rate of which a pirate would test his guesses is once per second, then even such a small cartridge would force a search lasting, on an average, more than two hundred million years. The coprocessor is, thus, able to determine reliably whether or not the cartridge it is reading was supplied by the software vendor to validate ownership or is a forgery, without exposing the information needed to create a successful forgery. The coprocessor will only store a decryption key (AK) for later use after the cartridge has been verified.

Figure 4 shows how the software vendor can construct a distributable software product, to be distributed on a magnetic medium or via communication link. The software vendor begins with three components,

- A. The application software,
- B. A software decryption key AK (known to the software vendor only), and
- C. Token data (a random number, known to the

software vendor only).

5 The software vendor, in a first function (F1), uses his own (secret) encryption key AK to encrypt the token data as well as critical parts (part 2) of the application software. The result of this encryption process becomes the encrypted token data [$E_{AK}(\text{TOKEN DATA})$] which will be distributed and the encrypted application software [$E_{AK}(\text{Software})$].

10 The noncritical parts of the application software (part 1) forms another component of the distributable software product.

15 Finally, the software decryption key (AK) is encrypted in a function F2, using the hardware vendor encryption CSK. The result of this process is the encrypted key [$E_{CSK}(AK)$] forms the last component of the distributable software product.

20 Figure 4 shows that function F2 is enclosed in a double rectangle to indicate that the encryption process takes place in the software vendor's coprocessor so that the hardware vendor's encryption key (which is contained in the coprocessor) is unknown to the software vendor.

25 In this fashion, the software vendor is prevented from acquiring knowledge of the hardware vendor's encryption keys. Software vendors can choose which hardware vendor's coprocessors they are willing to allow their keys to be installed on by preparing Encrypted Software Keys (EAKs) only with the coprocessors provided by trusted manufacturers. It is clearly in the best interests of the trusted hardware manufacturers not to destroy the effectiveness of their own product by revealing their own CSKs or using their own CSKs to reveal the software vendor's AKs. Accordingly, no "secret" data need be shared between the software and hardware vendor.

30 Comparing Figs. 5 and 6 reveals that, subsequent to the ARE process, the token cartridge 20 has had the token data erased, removed or overwritten so that it is no longer accessible. Fig. 6 shows the registers in the cartridge 20 as "empty". These registers are "empty" in the sense that the clear text token data which had resided there prior to the ARE process has been replaced by the data provided on the connector 23 (see Fig. 2) over the lines D3 and D4. This data is

35 meaningless to the validation process, and thus in terms of information content the registers in the token cartridge 20 are indeed "empty". Upon successful completion of the ARE process, the software decryption key AK, received by the coprocessor 15 in encrypted form $E_{CSK}(AK)$ has been decrypted, and in response to successful completion of the ARE, AK has been transferred (5) to the permanent memory 15P of the coprocessor 15. Fig. 6 also illustrates operation of the coprocessor 15 during the LDR. During this process, the software decryption key AK (5) is copied to the temporary memory 15T. The plain text software (file 2) is transferred to the host 10, where it is executable inasmuch as it is in plain text form. The encrypt-

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ed software (8) is transferred to the coprocessor 15 and resides in the temporary memory 15T. At that location, and employing the software decryption key AK, the encrypted software is decrypted (9) so that it is executable by the coprocessor 15.

Because the coprocessor 15 is secure, the clear text protected software, although it resides in the memory of the coprocessor 15, is unavailable to the user or anyone else. The software is, however, executable by the coprocessor 15, and the results of that execution can be communicated to the host 10. As a result, the composite computing system consisting of the host 10 and the coprocessor 15 cooperates in executing the software application package, the host or PC 10 executes the plain text portion, and the coprocessor 15 executes the protected portion, although if desired, the entire application could be protected and run solely on the coprocessor 15.

Figs. 3-6 illustrate an embodiment and application of the invention wherein the encrypted token data is provided to the user on the same medium which supports the application software, and while that medium could be magnetic, it is also within the contemplation of the invention that it could consist of a communication link.

Associating the encrypted token data with the software necessitates that to at least some extent the software distribution medium is unique or relatively unique (by the presence of the encrypted token data). As mentioned above, however, there are advantages if the distributable software medium can be generic and in that embodiment of the invention the encrypted token data does not appear on the software distribution medium (or is not associated when transmitted over a communication link with the application software). It is still essential, however, for the encrypted token data to be communicated to the coprocessor 15, because that information is essential to acquiring the right to execute. In this alternative embodiment of the invention, the hardware cartridge 20 is modified so that it includes at least a register dedicated to storing encrypted token data. The coprocessor 15, in addition to generating the challenge or query which has been described above, also generates a unique command and sufficient clocking pulses to transfer the encrypted token data, in toto, into the coprocessor 15. In this alternative embodiment of the invention then, the hardware cartridge 20 stores not only clear text token data (in the shift registers 120 and 220) but also stores encrypted token data in a third register (not shown in Fig. 2).

Fig. 7 is a flow diagram showing the functions executed in the host 10 in accordance with the invention; the services shown in Fig. 7 are those specific to the invention, other conventional services are not illustrated. As shown in Fig. 7, an initial determination, H1, determines whether or not the ARE process is requested. On first running, any particular application

5 package in accordance with the invention, this request would be present. Accordingly, function H2 is then requested to send the ARE request to the coprocessor 15. A loop is then entered consisting of H3 and H4 in which the host 10 services coprocessor requests until the coprocessor 15 indicates that it has completed the ARE process. On completion of the loop, processing loops back to function H1.

10 Subsequent to performing steps H1-H4, or at any time the LDR process is to be executed (on the second and any subsequent execution of the application software in accordance with the invention), function H1 directs processing to function H5. H5 determines if the LDR process is requested. If LDR is not requested, then function H6 determines if existing these services are requested; if not processing loops back to block H1. If an exit is requested then the processing shown in Fig. 7 terminates (END).

15 On the other hand, if at function H5 an LDR request was recognized, then function H7 is performed to send the LDR request to the coprocessor 15.

20 A loop is then entered consisting of functions H8 and H9 which is entirely similar to the loop H3-H4. When the coprocessor H9 indicates that LDR processing has been completed, then function H10 is performed to notify the corresponding program (for example the application program) in the host 10 that the LDR processing is completed.

25 When function H2 is performed, the coprocessor 15 is stimulated to execute its portion of the Acquire Right-To-Execute process. Those functions are shown in Fig. 8. Of the two privilege levels referred to previously, the ARE process is at the higher privilege level since its function is to protect dissemination of rights to execute for the software vendor's protection.

30 As shown in Fig. 8, function C1 requests the encrypted software decryption key from the host 10. The host 10 has access to this information from the software distribution medium (or communication link in the event the software is distributed in that form). Function C2 decrypts the encrypted software decryption key since it has access to CSK. CSK was installed by the manufacturer of the coprocessor 15 in the permanent 15P, at the time of manufacture. As has been indicated, there may be more than a single CSK, in that event the encrypted software decryption key file would contain a header, index or address to identify the appropriate CSK. Function C3 then requests the encrypted token data from the host. In the event the encrypted token data is associated on the same software distribution medium as is the application, the host 10 has access to it. Alternatively, the encrypted token data could be stored in the hardware cartridge 20; but as already described the host 10 may have access to the hardware cartridge 20, and if it does not, the coprocessor 15 has direct access to the hardware cartridge 20.

35 Function C4 decrypts the encrypted token data

using the already available software decryption key. The decrypted token data is then retained in the temporary memory 15T.

Function C5 then generates the random number which forms the token query. Function C6 combines the random number and the token data to simulate the token response and generates the computed response which is likewise retained in the temporary memory 15T.

Function C7 then requests the host to query the hardware cartridge 20 with the random number (the token query). Of course, if the coprocessor 15 has direct access to the hardware cartridge 20, this function is implemented directly rather than indirectly through the host 10. Function C8 then requests the host to supply the token response, which is also stored in the temporary memory 15T. Function C9 then compares the actual response with the computed or expected response. Function C10 is a branch based on the comparison of function C9. If the computed and actual response of the token compare favorably (typically they would be equivalent) then that is taken as evidence or an authentic token. Function C13 thereafter moves the software decryption key into permanent storage (5). Function C14 then passes a message through the host 10 to inform the user that a successful ARE process has been completed and provides information to the host 10 to identify the location of the software decryption key corresponding to this application. Thereafter, the ARE process terminates.

On the other hand, if the comparison of the actual and computed response of the token was unsuccessful (for example the responses were unequal) then rather than executing functions C13 and C14, function C11 is executed to pass onto the user information that the Right-To-Execute was not acquired. It should be noted that in this event, although the software decryption key is available in the temporary memory 15T, it is not transferred into the permanent memory 15P. Accordingly, the software decryption key is unusable and the application package will not execute.

Fig. 9A illustrates the functions performed in the host during a typical LDR process. Initiating the LDR process is function H11 which starts the application program. On determining that the program is protected, function H12 is performed requesting an LDR order to DOS with the information to determine the location of AK, e.g., the location in the permanent memory of the coprocessor 15 at which the necessary software decryption key is located. This information had been provided to the host and stored with the application program via function C14 at the successful conclusion of the preceding ARE process.

Function H13 then is merely a delay waiting for notification from the DOS service (see Fig. 7). On that notification (H10 - Fig. 7), function H14 is performed to execute the application using code executing in the coprocessor 15 as needed. In order to see

how the protected software is executed in the coprocessor, reference is made to Fig. 9B.

As shown in Fig. 9B, when the coprocessor 15 receives an LDR request, function C15 is performed to initiate the functions shown in Fig. 9B. The LDR request is initiated at function H7 (see Fig. 7). Function C16 requests the host to provide information to locate AK. That request is passed through the DOS service (Fig. 7) which responds with the information provided by function H8. With the index information, function C17 obtains a copy of the software decryption key from the permanent storage and loads it into the temporary memory 15T of the coprocessor 15. Function C18 then requests the protected software from the host. The protected software, in encrypted form, is retained in the temporary memory and decrypted, at function C19, using the software decryption key obtained at function C17. The result of function C19 is the clear text version of the protected software; this is retained in the temporary memory 15T of the coprocessor 15. The protection afforded by the logical and physical security of the coprocessor 15 prevents divulgence of the clear text portion of the protected software to the user or anyone else. Function C20 then notifies the host that the LDR process has been completed. Function C21 then executes the decrypted software. Functions C17-C19 are also considered to represent the higher of the two privilege levels since they deal with use of the right to execute, AK. Function C21, on the other hand, is an example of the lower of the two privilege levels. It should be apparent to those skilled in the art that functions H14 and C21 can cooperate using a variety of techniques so that the net result is complete execution of the application software. It is within the scope of the invention that all of the application software is protected. During execution of the protected software by the coprocessor 15, only the results of that execution are provided to the host 10. It should be apparent that the results of the execution, which is available to the user, is inadequate as a basis on which to reconstruct clear text or the protected software.

The invention then meets the objects previously stated. More particularly, the software distribution medium can be freely copied or backed up, however, it will not execute except on composite systems including a coprocessor such as the coprocessor 15. Even then, it will only execute on a composite system in which the coprocessor 15 has available to it the necessary software decryption key. Furthermore, in order for the coprocessor 15 to obtain access to the software decryption key, and retain that information in permanent memory, an authentic cartridge 20 is necessary; the cartridge 20 is arranged so that it is difficult to forge or simulate and is usable only on one occasion. The necessary consequence is that a suitable hardware cartridge 20 can authorize one and only one coprocessor 15. While only a single copro-

cessor 15 will be authorized, that coprocessor 15 is in essence portable so that it could be used with any suitable host 10 to run the protected software. Thus the software vendor achieves the desirable result of separating the protected application software from the right to execute it. His customer, the user, can make as many copies of the protected software as he desires but can only execute it, at any one time, on a single composite computing system.

CONDITIONING RIGHTS TO EXECUTE

The software asset protection mechanism, as briefly described above, installs an unconditioned right to execute in the coprocessor 15. However, it is one of the features of the invention that the right to execute can be conditioned, and examples of conditions are terminal dates and times, or numbers of executions. Figs. 10 A-C illustrate the case where the protected portion of the application file includes a criterion for execution, e.g., if the terminal date and/or time is later than the present date then execution is allowed. On presentation of a distribution set as shown in Fig. 10A to the combined processing system of the host 10 and the coprocessor 15, the installation of the right to execute proceeds exactly as was described above. The coprocessor 15, via the agency of the host 10, verifies (and destroys) the token 20 by comparing its plain text contents (T_1) with a decrypted version of the file $E_{AK}(T_1)$ read from the disk 46. On verifying the authenticity and the previously unused nature of the token 20, the coprocessor 15 stores the software decryption key AK in the permanent memory 15P. The conditions of execution can be stored in the same file as the AK and can be installed at the same time as AK. In the case shown in Fig. 10A, the coprocessor 15 stores the datum which can be interpreted as a terminal date and/or time. This interpretation will be performed by the protected part of the application on any occasion of its use. The terminal date storage is associated with the software decryption key AK, as shown in Fig. 10B. More particularly, Fig. 10B is entirely identical to Fig. 10A except that it shows the software decryption key AK, and the terminal date and/or time, stored in the coprocessor 15's permanent memory as well as indicating that the token 20 has been depleted. Thereafter, each time the protected application is run on the coprocessor 15, prior to authorizing execution, the application uses the criterion stated in the encrypted application file that the current date and/or time not be later than the terminal date and/or time and only authorizes execution in the event the criterion is met. The current date and/or time is supplied on demand by the coprocessor 15 to the executing application.

Fig. 10C is similar to Fig. 10B except that the conditions stated in the encrypted application key file gives the remaining number of authorized executions.

On installation of the right to execute, the software decryption key AK is associated with a count C; each time execution of the application is requested, the contents of the count C is tested against the criterion that the number C of authorized executions remaining is greater than zero. The count C is then decremented. Only so long as C is greater than zero will execution be authorized.

It may be advisable, regardless of whether or not the condition is a terminal date and or time, or a number of executions, to provide the coprocessor 15 with instructions to delete the associated software decryption key AK, once the initial conditions are no longer met. Thus, the software decryption key AK is automatically removed from the permanent memory and thus the right to execute is deleted. Further details of conditioning rights to execute are described in the EP-A-0,268,139 which is incorporated herein by reference.

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Claims

1. A method of securing protected software (46) against unauthorized use without perturbing existing channels of software distribution and, at the same time, not interfering in users' creation of unlimited backup copies of protected software, said method comprising the steps of:
 - a) providing to a user a physically secure coprocessor (15) and coupling said coprocessor to a user host computer (10) to support bidirectional communication therebetween to create a composite computing system including said host computer and said coprocessor;
 - b) providing logical security to said coprocessor by:
 - 1) providing a first privilege level including first level secure memory (151) and first level operating instructions (152), secured against access or variation by said user, for executing protected software;
 - c) distributing protected software (46) in a form in which at least a portion is inexecutable by said host computer;
 - d) distributing a further tangible element (20) distinct from said protected software representing a right to execute said protected software;
 - e) providing said composite computing system access to said protected software and to said further tangible element;
2. characterised in that:
 - step b) further comprises the step of:
 - 2) providing a second privilege level including second level secure memory (153) and second level operating instructions (152), secured against access or variation by said user

or any author of protected software, for controlling authorization for execution of said protected software by said first privilege level;

in that, in step c), the protected software distributed is executable by said coprocessor but only with authorization by said second privilege level;

and in that the method further comprises the steps of:

f) verifying authenticity of said further tangible element by said coprocessor at said second privilege level;

g) altering said second level secure memory in a distinctive fashion to reflect a determination by said second privilege level of authenticity of said tangible element; and

h) executing said protected software so long as said alteration of said second privilege level secure memory is detected and denying said request if said alteration is not present.

2. A method as claimed in claim 1 wherein:

said software (46) as distributed in said step c) has at least a portion encrypted;

said second privilege level subsequent to performance of said step f) has access to a decryption key (AK); and

said step h) comprises performing the following steps on each subsequent request by said user to execute said protected software:

responding, at said second privilege level, to check for said alteration of said second level secure memory, and, if said alteration is present, honouring said request by:

a) initiating decryption of said protected software and storage of said decrypted software in said first privilege level secure memory,

b) authorizing execution of said decrypted software by said first privilege level and initiating operation of said first privilege level,

and, if said alteration is not present, refusing said request.

3. A method as claimed in claim 1 wherein said protected software (46) as distributed in said step c) includes a portion which is encrypted under a software key which itself is distributed encrypted under a different key, and in which said second privilege level has access to said different key so that said encrypted software key can be decrypted by said second privilege level, and in which said step g) includes:

1) decrypting said software key with said second key; and

2) altering said second privilege level secure memory by writing said software key therein.

4. A method as claimed in claim 3 in which said com-

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posite computing system is provided information with which to verify said tangible element and in which said step d) includes:

1) providing said tangible element with physical security;

2) providing said tangible element with electronic storage having a destructive read function; and

3) storing a verifiable entity in said electronic storage.

5. A method as claimed in claims 1 - 4, wherein execution of said protected software (46) is only allowed if a condition is met, the method further comprising the steps of:

a) providing to said coprocessor a statement of at least one condition;

b) providing for storage in said secure memory data associated with said condition along with storage of said software key;

c) requiring said coprocessor to access said statement and said data prior to authorizing any use of said software key or said protected software; and

d) further requiring said coprocessor to compare said statement and said data to determine if said condition is met, and authorizing use if, and only if, said condition is met.

6. A method as claimed in claim 5 wherein:

e) said statement represents authorization for execution of said protected application no more than N times, wherein N is any integer;

f) said secure memory data represents either a count C which is incremented each time said coprocessor authorizes execution of said protected software or said integer N decremented each time said coprocessor authorizes execution of said protected software; and

g) said coprocessor either compares C to N or compares N to zero and said coprocessor authorizes execution if and only if $N > C$ or $N > 0$.

7. The method of claim 5 wherein:

e) said statement represents authorization for execution of said protected software until a current date and/or time reaches a terminal date and/or time;

f) said secure memory data represents a terminal date and/or time; and

g) said coprocessor compares a current date and/or time with said terminal date and/or time and authorizes execution if, and only if said current date and/or time precedes said terminal date and/or time.

Patentansprüche

1. Verfahren zur Sicherung geschützter Software (46) gegen unbefugte Benutzung, ohne daß existierende Kanäle zur Softwareverteilung zerstört werden und gleichzeitig ohne daß die unbeschränkte Herstellung von Sicherheitskopien der geschützten Software durch den Benutzer gestört wird, wobei das Verfahren die Schritte umfaßt:

a) Bereitstellen eines physisch sicheren Co-
prozessors (15) und Koppeln des Coprozes-
sors mit einem Wirtsrechner (host) (10) des
Benutzers, um zwischen diesen Computern
einen bidirektionalen Informationsaustausch
mit dem Ziel zu unterhalten, ein zusammen-
gesetztes Datenverarbeitungssystem zu er-
zeugen, das den Wirtsrechner und den Co-
prozessor umfaßt;

b) Bereitstellen logischer Sicherheitsvorkeh-
rungen für den Coprozessor durch:

1) Bereitstellen einer ersten privilegier-
ten Ebene, zur Ausführung der geschützten
Software, die einen sicheren Speicher (151)
der ersten Ebene und Operationsbefehle der
ersten Ebene (152) enthält, welche gegen Zu-
griff und Veränderung durch den Benutzer
gesichert sind;

c) Verteilen der geschützten Software (46) in
einer Form, in welcher zumindest ein Teil
durch den Wirtsrechner unausführbar ist;

d) Verteilen eines weiteren greifbaren, von
der geschützten Software getrennten Ele-
mentes (20), das das Recht darstellt, die ge-
schützte Software auszuführen;

e) Herstellen des Zugriffs des zusammenge-
setzten Datenverarbeitungssystems auf die
geschützte Software und auf das weitere
greifbare Element;

dadurch gekennzeichnet, daß :

Schritt b) weiterhin den Schritt umfaßt:

2) Bereitstellen einer zweiten privile-
gierten Ebene zur Überwachung des Befugnis-
ses zur Ausführung der geschützten Software
durch die erste privilegierte Ebene, enthaltend ei-
nen sicheren Speicher der zweiten Ebene (153)
und Operationsbefehle der zweiten Ebene (152),
welche gegen Zugriff und Veränderung durch
den Benutzer oder irgendeinen Autor von ge-
schützter Software gesichert sind;

daß die geschützte Software, wie sie ent-
sprechend Schritt c) verteilt wird, durch den Co-
prozessor ausführbar ist, aber nur über die Er-
mächtigung durch die zweite privilegierte Ebene;
und daß das Verfahren des weiteren die Schritte
umfaßt:

f) Überprüfen der Authentizität des weiteren
greifbaren Elementes durch den Coprozessor

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auf der zweiten privilegierten Ebene;

g) Verändern des sicheren Speichers der
zweiten Ebene auf eine besondere Weise, da-
mit eine Feststellung der Authentizität des
greifbaren Elementes durch die zweite privile-
gierte Ebene wiedergegeben wird und

h) Ausführen der geschützten Software so-
lange, wie die Veränderung des sicheren
Speichers der zweiten privilegierten Ebene
erkannt wird und Ablehnen einer entspre-
chenden Anforderung, wenn diese Verände-
rung nicht vorhanden ist.

2. Verfahren nach Anspruch 1, dadurch gekenn-
zeichnet, daß
die Software (46), wie sie entsprechend Schritt c)
verteilt wird, mindestens einen verschlüsselten
Abschnitt besitzt;

die zweite privilegierte Ebene nachfolgend auf
den Schritt f) Zugriff auf einen Dechiffrierteschlüs-
sel (AK) hat, und

Schritt h) bei jeder folgenden Anforderung des
Benutzers nach Ausführung der geschützten
Software die folgenden Schritte ausführt:

Reagieren auf der zweiten privilegierten
Ebene, um die Veränderung des sicheren Spei-
chers der zweiten Ebene zu überprüfen, und
wenn diese Veränderung vorhanden ist, Aner-
kennen der Programmanforderung durch:

a) Starten der Entschlüsselung der geschütz-
ten Software und Speichern der entschlüs-
selten Software im sicheren Speicher der er-
sten Ebene,

b) Erteilen der Befugnis zur Ausführung der
entschlüsselten Software durch die erste pri-
vilegierte Ebene und Starten der Arbeit der er-
sten privilegierten Ebene

und wenn diese Veränderung nicht vor-
handen ist, Ablehnen der Programmanforderung.

3. Verfahren nach Anspruch 1, dadurch gekenn-
zeichnet, daß die geschützte Software (46), wie
sie in Schritt c) verteilt wird, einen Abschnitt ent-
hält, der mittels eines Softwareschlüssels chif-
friert worden ist, welcher wiederum selbst mit ei-
nem anderen Schlüssel verschlüsselt verteilt
wird und wobei die zweite privilegierte Ebene Zu-
griff auf diesen anderen Schlüssel hat, so daß
der verschlüsselte Softwareschlüssel durch die
zweite privilegierte Ebene entschlüsselt werden
kann, und wobei der Schritt g) enthält:

1) Entschlüsseln des Softwareschlüssels mit
dem zweiten Schlüssel und

2) Verändern des sicheren Speichers der
zweiten privilegierten Ebene dadurch, daß
der Softwareschlüssel dort eingeschrieben
wird.

4. Verfahren nach Anspruch 3, dadurch gekennzeichnet, daß das zusammengesetzte Datenverarbeitungssystem mit Informationen versehen wird, mit denen das greifbare Element überprüft werden kann, und wobei der Schritt d) enthält:

- 1) Versehen des greifbaren Elementes mit physischen Sicherheitsvorkehrungen;
- 2) Versehen des greifbaren Elementes mit einem elektronischen Speicher, der eine zerstörende Lesefunktion besitzt, und
- 3) Speichern einer überprüfbaren Einheit in dem elektronischen Speicher.

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5. Verfahren nach den Ansprüchen 1 bis 4, dadurch gekennzeichnet, daß die Ausführung der geschützten Software (46) nur dann gestattet wird, wenn eine Bedingung erfüllt ist, wobei das Verfahren weiterhin folgende Schritte umfaßt:

- a) Bereitstellen einer Anweisung mit mindestens einer Bedingung für den Coprozessor;
- b) Bereitstellen von mit dieser Bedingung verbundenen Daten zum Speichern in dem sicheren Speicher zusammen mit dem Speichern des Softwareschlüssels;
- c) Anweisen des Coprozessors, vor jeder Erteilung einer Befugnis zur Nutzung des Softwareschlüssels oder der geschützten Software auf die Anweisung und die Daten zuzugreifen, und
- d) des weiteren Anweisen des Coprozessors, die Anweisung und die Daten zu vergleichen, um daraus zu bestimmen, ob die Bedingung erfüllt ist, und Erteilen der Benutzungsbefugnis nur dann, wenn die Bedingung erfüllt ist.

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6. Verfahren nach Anspruch 5, dadurch gekennzeichnet, daß

- e) die Anweisung eine Benutzungsbefugnis zur Ausführung der geschützten Anwendung nicht häufiger als N-mal repräsentiert, wobei N eine ganze Zahl ist;
- f) die Daten des sicheren Speichers entweder einen Zählerstand C repräsentieren, der jedes Mal erhöht wird, wenn der Coprozessor die Befugnis zur Ausführung der geschützten Software erteilt, oder aber die ganze Zahl N, die jedes Mal verringert wird, wenn der Coprozessor die Befugnis zur Ausführung der geschützten Software erteilt, und
- g) der Coprozessor entweder C mit N oder N mit Null vergleicht und daß der Coprozessor die Befugnis zur Ausführung nur dann erteilt, wenn $N > C$ oder $N > 0$ ist.

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zu einem Zeitpunkt repräsentiert, an dem das momentane Datum und/oder die Zeit ein Beendigungsdatum und/oder eine Beendigungszeit erreicht;

f) die Daten des Sicherungsspeichers ein Beendigungsdatum und/oder eine Beendigungszeit repräsentieren, und

g) der Coprozessor das momentane Datum und/oder die Zeit mit dem Beendigungsdatum und/oder der Beendigungszeit vergleicht und die Befugnis zur Ausführung nur dann erteilt, wenn das momentane Datum und/oder die Zeit vor dem Beendigungsdatum und/oder der Beendigungszeit liegen.

Revendications

1. Procédé pour interdire toute utilisation non autorisée d'un logiciel protégé (46) sans perturber les circuits de distribution de logiciels existants et sans entraver la liberté de création, par les utilisateurs, d'un nombre illimité de copies de sauvegarde du logiciel protégé, ledit procédé comprenant les étapes consistant à :
 - a) fournir à l'utilisateur un coprocesseur protégé physiquement (15) et coupler ledit coprocesseur à un processeur central utilisateur (10) pour assurer une communication bidirectionnelle entre les deux, afin de créer un système de traitement composite comprenant ledit processeur central et ledit coprocesseur;
 - b) assurer la protection logique du dit coprocesseur en :
 - 1) prévoyant un premier niveau de privilège incluant une mémoire protégée de premier niveau (151) et des instructions d'exploitation de premier niveau (152), protégé contre tout accès ou altération par ledit utilisateur, pour exécuter le logiciel protégé;
 - c) distribuer le logiciel protégé (46) sous une forme sous laquelle au moins une partie est inexécutable par ledit processeur central;
 - d) distribuer un autre élément tangible (20) distinct du dit logiciel protégé représentant un droit d'exécution du dit logiciel protégé;
 - e) assurer l'accès au système de traitement composite par ledit logiciel protégé et ledit autre élément tangible;

caractérisé en ce que:

l'étape b) comprend, de plus, l'étape consistant à:

- 2) prévoir un second niveau de privilège incluant une mémoire protégée de second niveau (153) et des instructions d'exploitation de second niveau (152), protégé contre tout accès ou altération par ledit utilisateur ou tout auteur de logiciels protégés, pour contrôler l'autorisation d'exécution du dit logiciel protégé par ledit pre-

mier niveau de privilège; et en ce que, au cours de l'étape c), le logiciel protégé distribué est exécutable par ledit coprocesseur uniquement avec l'autorisation du dit second niveau de privilège; et en ce que le procédé comprend, de plus, les étapes consistant à:

f) faire vérifier l'authenticité du dit autre élément tangible par ledit coprocesseur au dit second niveau de privilège;

g) modifier ladite mémoire protégée de dit second niveau de manière distinctive pour refléter la détermination par ledit second niveau de privilège de l'authenticité du dit élément tangible; et

h) exécuter ledit logiciel protégé tant que ladite modification de ladite mémoire protégée de second niveau est détectée, et rejeter ladite demande d'exécution si ladite modification n'est pas présente.

2. Procédé selon la revendication 1, dans lequel : ledit logiciel (46), distribué conformément à l'étape c), comprend au moins une partie cryptée; ledit second niveau de privilège a, à l'issue de l'exécution de ladite étape f), accès à une clé de déchiffrage (AK); et ladite étape h) comprend l'exécution des étapes suivantes à chaque demande ultérieure par l'utilisateur d'exécution du dit logiciel protégé:

répondre, au dit second niveau de privilège, pour vérifier ladite modification de ladite mémoire protégée de second niveau, et, si la modification est effective, honorer ladite demande en :

a) lançant le déchiffrage du dit logiciel protégé et la mémorisation du dit logiciel déchiffré dans ladite mémoire protégée de premier niveau,

b) autorisant l'exécution du dit logiciel déchiffré par ledit premier niveau de privilège, et en lançant l'exploitation du dit premier niveau de privilège,

et rejeter ladite demande si ladite modification n'est pas effective.

3. Procédé selon la revendication 1, dans lequel ledit logiciel protégé (46), distribué conformément à l'étape c), comprend une partie cryptée selon une clé logicielle qui est elle-même distribuée cryptée selon une autre clé, et dans lequel ledit second niveau de privilège a accès à ladite autre clé afin de permettre le déchiffrage de ladite clé logicielle cryptée par ledit second niveau de privilège, et dans lequel ladite étape g) comprend les étapes consistant à:

1) déchiffrer ladite clé logicielle à l'aide de ladite seconde clé; et

2) modifier ladite mémoire protégée de second niveau en chargeant ladite clé logicielle dans celle-ci.

5 4. Procédé selon la revendication 3, dans lequel ledit système de traitement composite dispose d'informations qui lui permettent de vérifier ledit élément tangible, et dans lequel ladite étape d) comprend les étapes consistant à:

1) assurer la protection physique du dit élément tangible;

2) prévoir, dans ledit élément tangible, une mémoire électronique ayant une fonction de lecture destructive; et

3) mémoriser une entité vérifiable dans ladite mémoire électronique.

10 5. Procédé selon l'une quelconque des revendications 1 à 4, dans lequel l'exécution du dit logiciel protégé (46) n'est autorisée que lorsqu'une condition est remplie, le procédé comprenant, de plus, les étapes consistant à :

a) fournir au dit coprocesseur au moins une condition sous forme d'instruction;

b) prévoir la mémorisation dans ladite mémoire protégée de données associées à ladite condition lors de la mémorisation de ladite clé logicielle;

c) demander au coprocesseur d'accéder à ladite instruction et aux dites données avant d'autoriser l'utilisation de ladite clé logicielle ou dit logiciel protégé; et

d) demander au coprocesseur de comparer ladite instruction et les dites données pour déterminer si ladite condition est remplie, et n'autoriser l'utilisation que si et seulement si ladite condition est remplie.

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6. Procédé selon la revendication 5, dans lequel :

e) ladite instruction représente l'autorisation d'exécuter ladite application protégée au plus N fois, où N est un nombre entier;

f) les données dans ladite mémoire protégée représente soit un compte C incrémenté à chaque fois que le coprocesseur autorise l'exécution du dit logiciel protégé, soit ledit entier N décrémenté à chaque fois que le coprocesseur autorise l'exécution du dit logiciel protégé; et

g) ledit coprocesseur compare C à N, ou compare N à zéro, et autorise ladite exécution si et seulement si $N>C$ ou $N>0$.

7. Procédé selon la revendication 5, dans lequel :

e) ladite instruction représente l'autorisation d'exécuter ledit logiciel protégé jusqu'à ce qu'une date et/ou une heure courante(s) atteigne(nt) une date et/ou une heure limite(s);

f) les dites données dans la mémoire protégée représentent une date et/ou une heure limite(s); et

g) ledit coprocesseur compare une date/ou une heure courante(s) avec ladite (les dites) date et/ou heure limite(s) et autorise ladite exécution si et seulement si ladite (les dites) date et/ou heure courante(s) précède(nt) ladite (les dites) date et/ou heure limite(s).

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FIG. 1

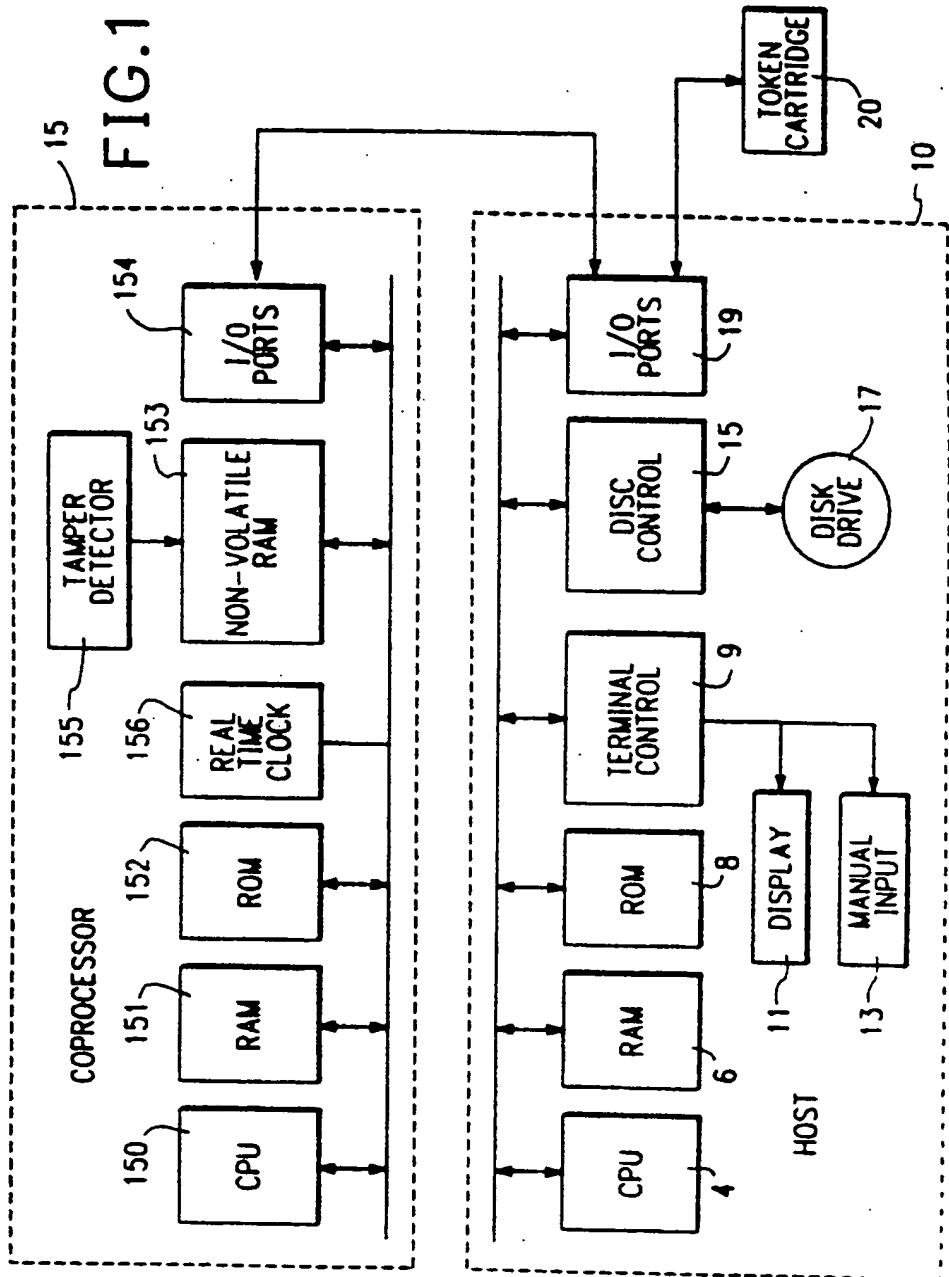
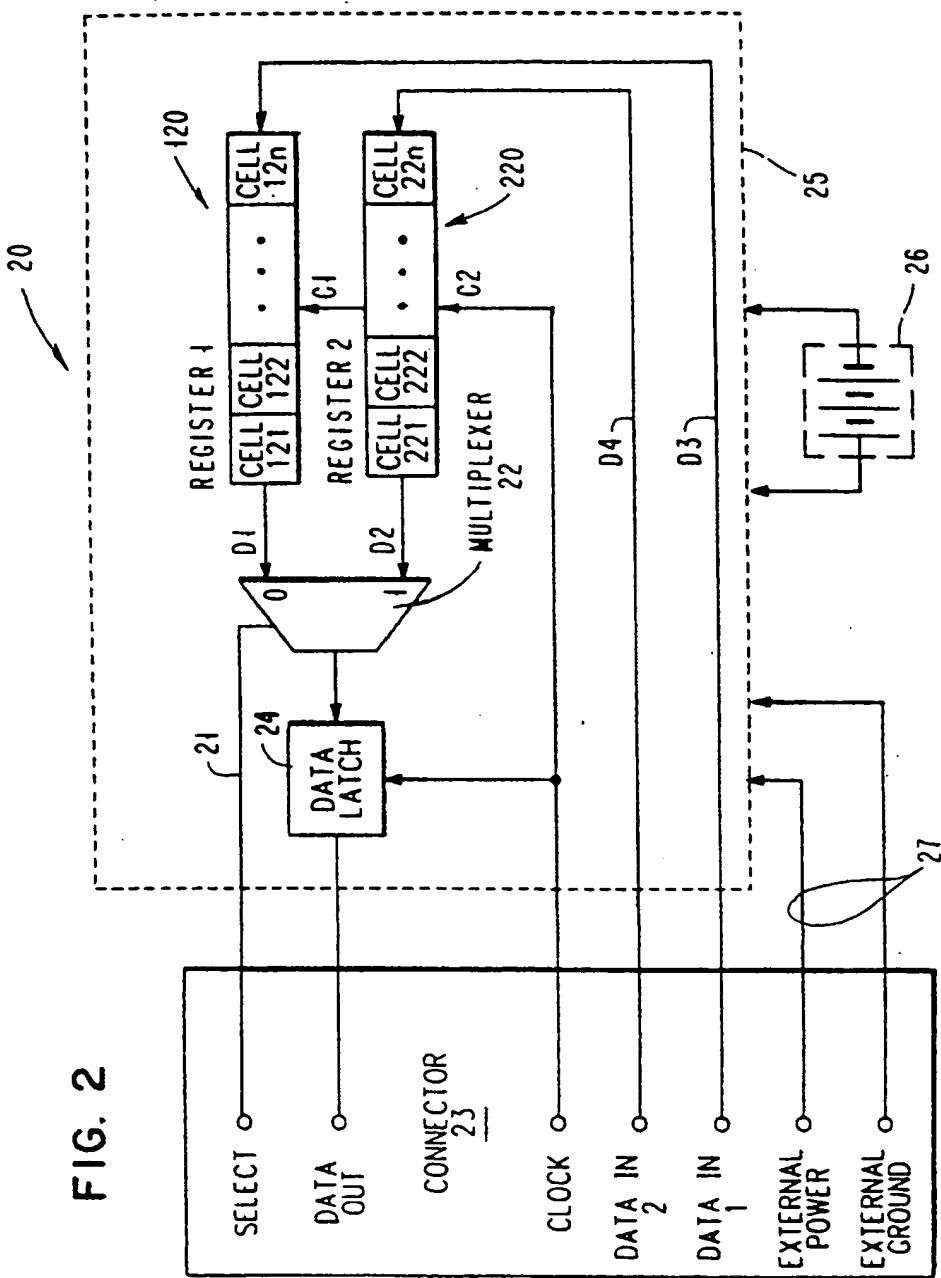


FIG. 2



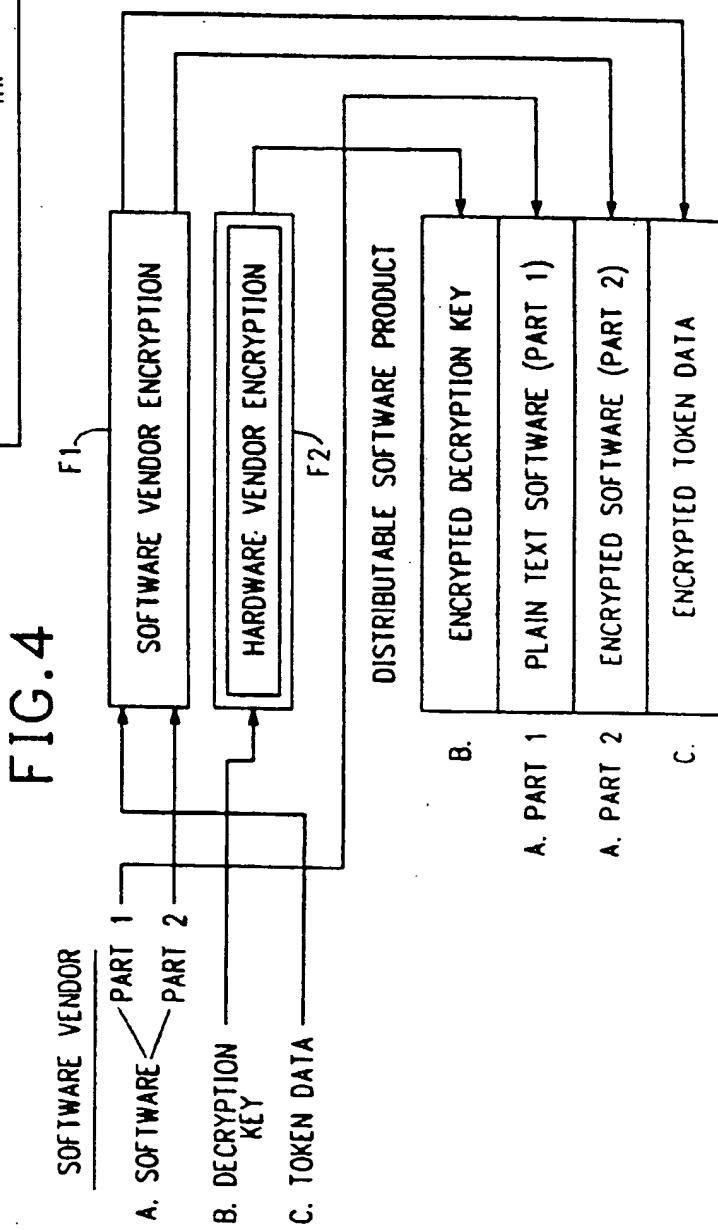
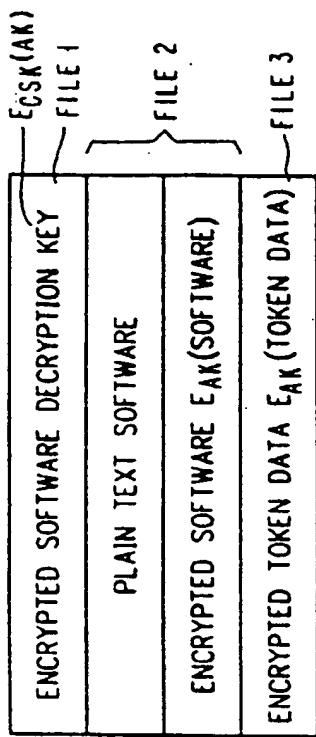
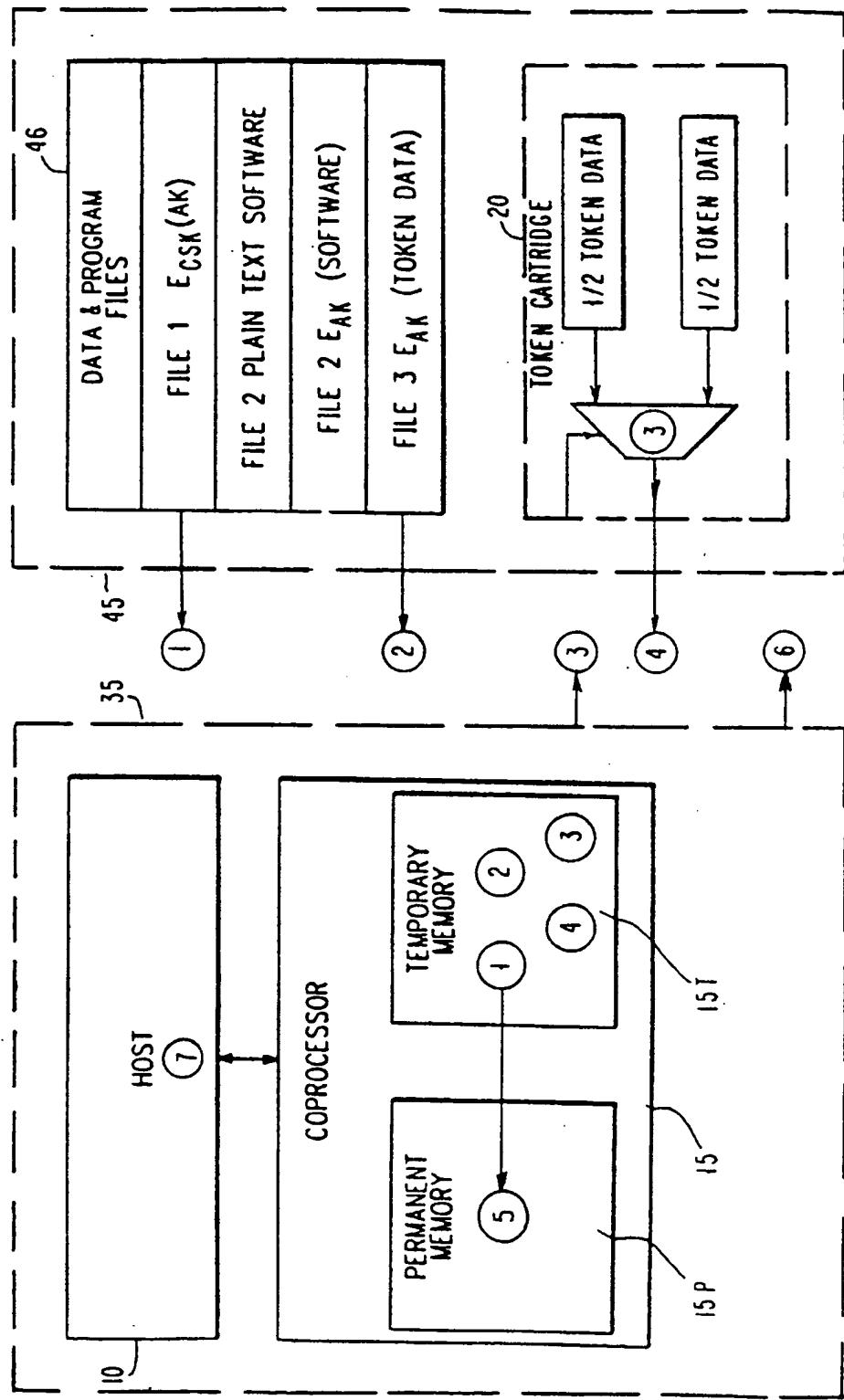


FIG. 5 SOFTWARE DISTRIBUTION PACKAGE FOR COMPOSITE COMPUTING SYSTEM



COMPOSITE COMPUTING
SYSTEM

FIG. 6 SOFTWARE DISTRIBUTION PACKAGE

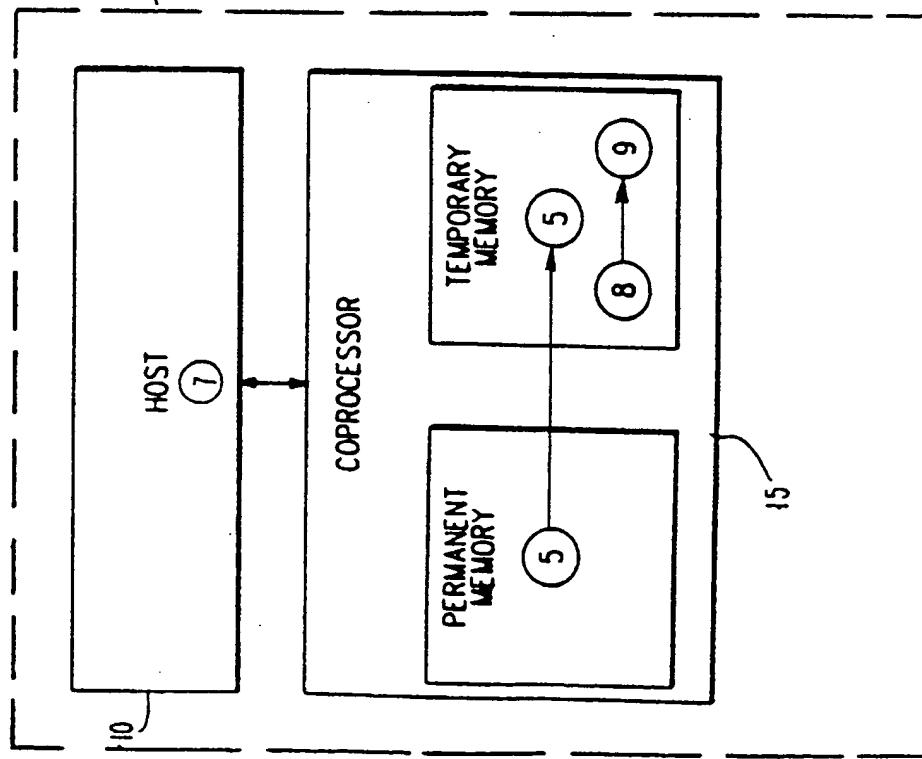


FIG.7
**DOS SERVICE
IN HOST**

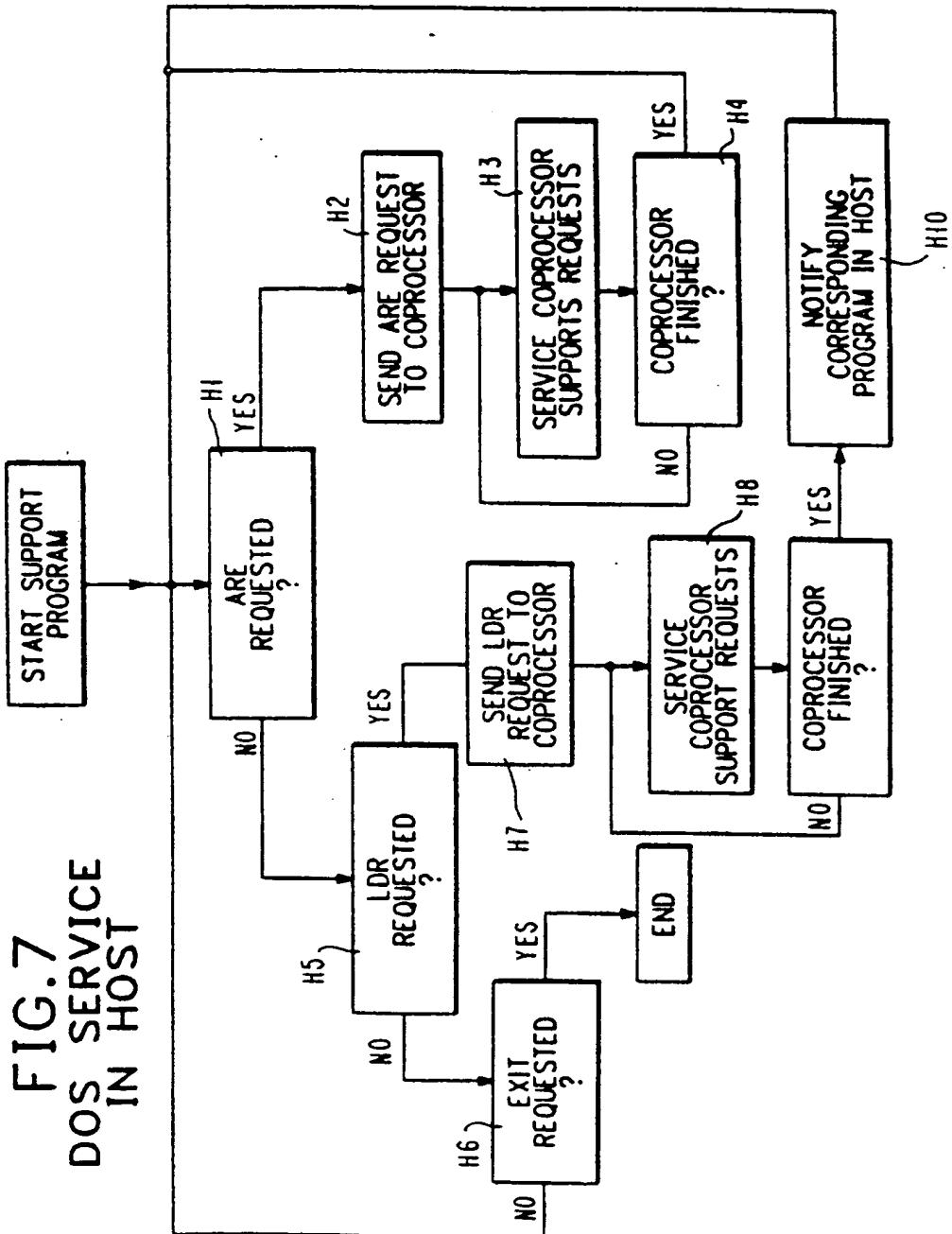


FIG. 8

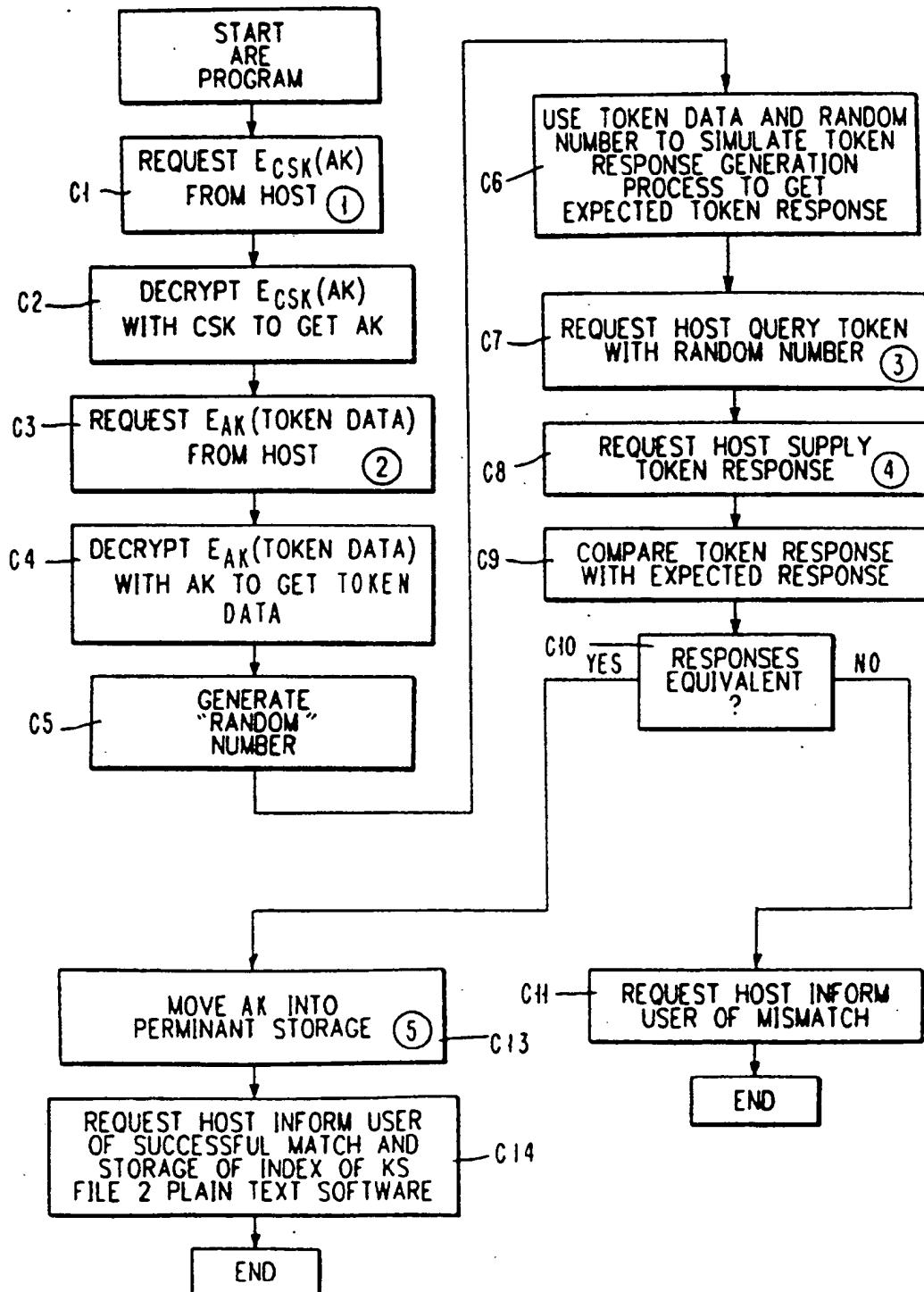


FIG. 9A

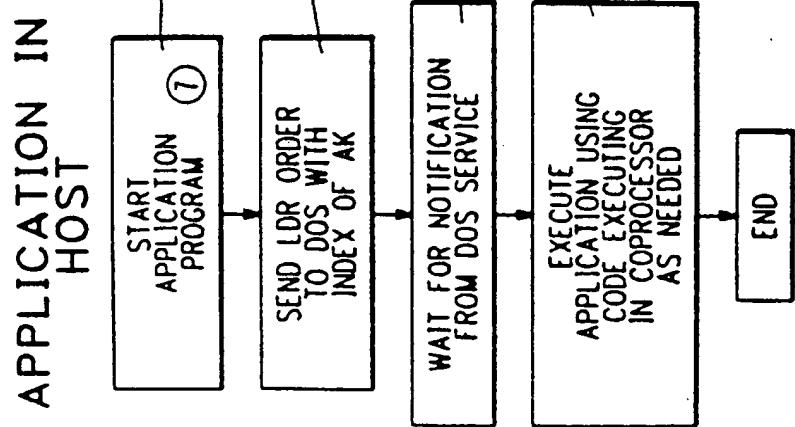


FIG. 9B

APPLICATION IN COPROCESSOR

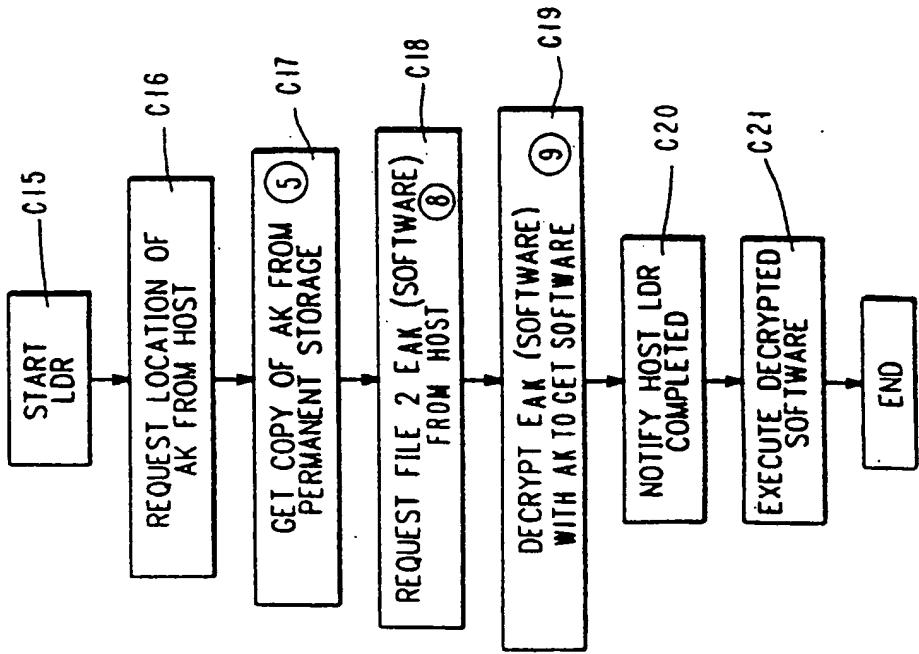


FIG. 10A

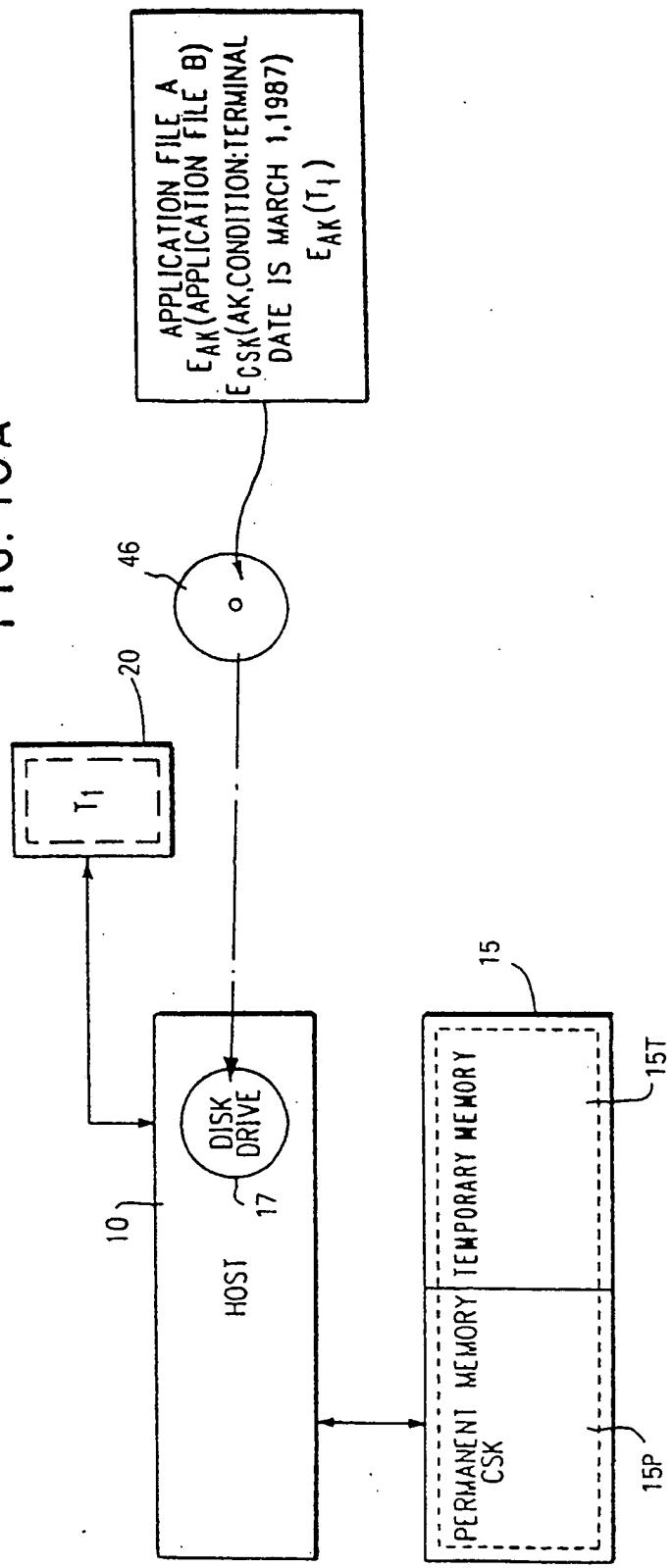


FIG. 10B

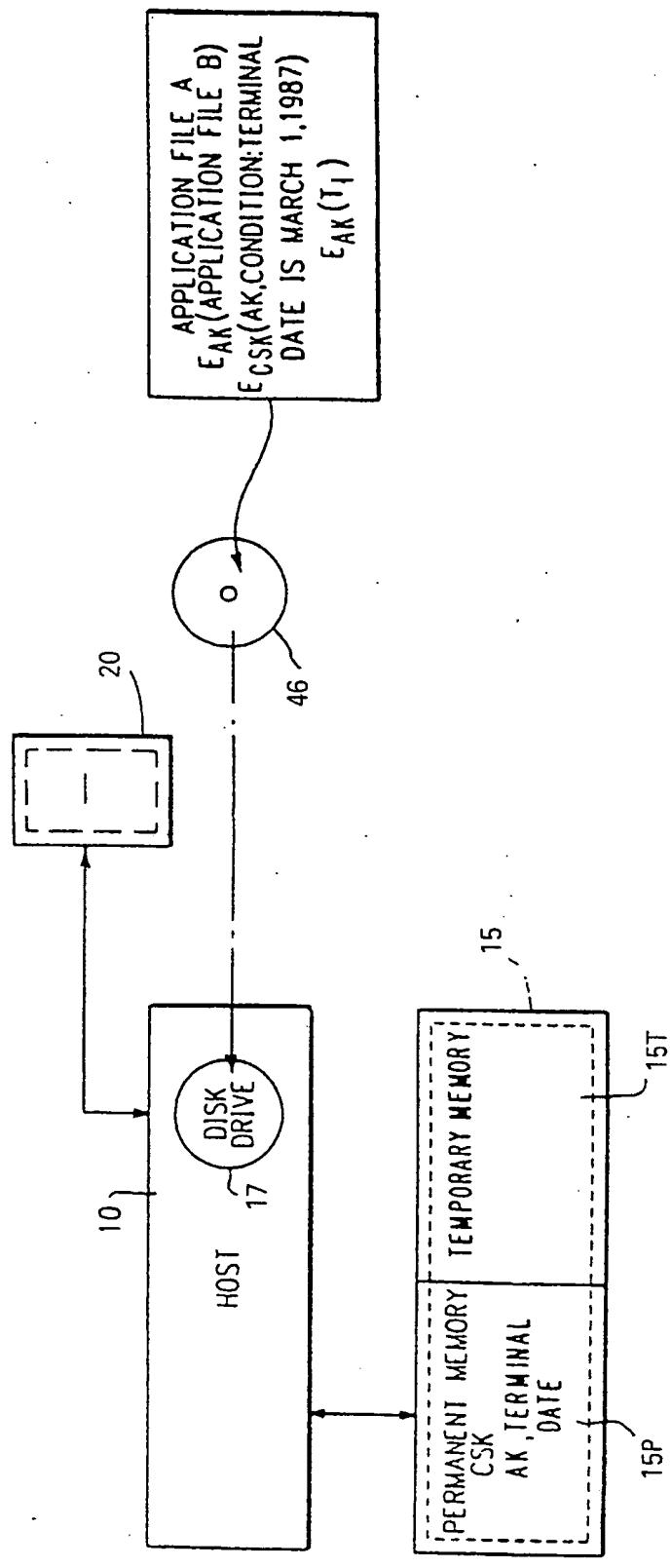


FIG. 10 C

